

## **300mA Low Dropout (LDO) Linear Regulator**

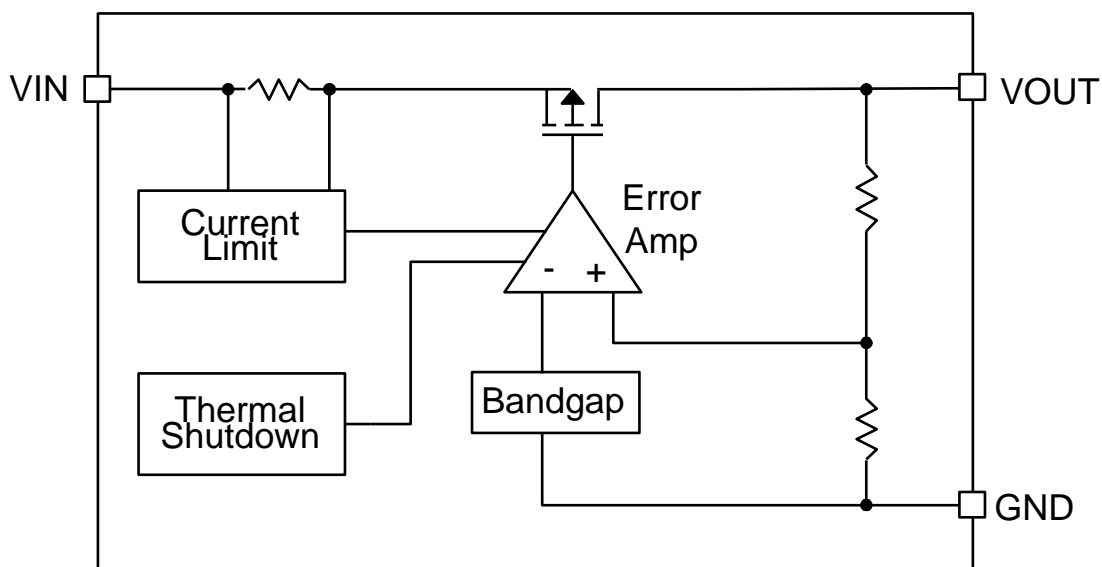
### ❖ GENERAL DESCRIPTION

The AX6601 is a 300mA, fixed output voltage, low dropout linear regulator. The Device includes pass element, error amplifier, band-gap, current-limit, and thermal shutdown circuitry. The characteristics of low dropout voltage and less quiescent current make it good for some critical current application, for example, some battery powered devices. The typical quiescent current is approximately 90 $\mu$ A. Due to internal flexible design, result in extensively fixed output voltage versions and make it convenient to use for applications. Built-in current-limit, short-circuit protection and thermal-shutdown functions prevent any fault condition from IC damage.

### ❖ FEATURES

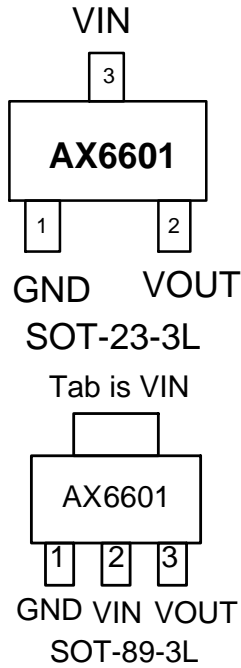
- Input voltage range is up to 6V
- Dropout voltage 200mV at 150mA output current
- Guaranteed 300mA output current
- Low quiescent current 90 $\mu$ A (typ.)
- Fixed Output voltage is 1.5V, 1.8V, 2.5V, 2.8V, 3.0V and 3.3V.
- Fast transient response
- Current limit ,short-circuit protection and thermal shutdown protection
- Available in the SOT-23-3L and SOT-89-3L Packages
- RoHS and Halogen free compliance.

### ❖ BLOCK DIAGRAM



**❖ PIN ASSIGNMENT**

The packages of AX6601 are SOT-23-3L and SOT-89-3L; the pin assignment is given by:



Name	Description
GND	Ground
VIN	Voltage input
VOUT	Output Voltage

**❖ ORDER/MARKING INFORMATION**

Order Information	Top Marking (SOT-89-3L)														
<p><b>AX6601- X X X X</b></p> <p>Output voltage Package Packing</p> <p>15 : 1.5V R : SOT-23-3L Blank:Tube</p> <p>18 : 1.8V F : SOT-89-3L A : Taping</p> <p>25 : 2.5V</p> <p>28 : 2.8V</p> <p>30 : 3.0V</p> <p>33 : 3.3V</p>	<p><b>6 6 0 1</b></p> <p><b>L Y W X</b> → ID Code: internal</p> <p>Output Type → <b>WW: 01~26(A~Z)</b> 27~52(a~z)</p> <p>→ <b>Year: 8=2018</b> 9=2019 B=2020 C=2021 D=2022 Z=2044</p> <p>A:1.5V B:1.8V C:2.5V D:2.8V E:3.0V F:3.3V</p>														
Top Marking (SOT-23-3L)	Appendix														
<p><b>L L Y W X</b> → ID Code: internal</p> <p>→ <b>WW: 01~26(A~Z)</b> 27~52(a~z)</p> <p>→ <b>Year: 8=2018</b> 9=2019 B=2020 C=2021 D=2022 Z=2044</p> <p>Identification code</p>	<table border="1"> <thead> <tr> <th>Part Number</th> <th>Identification Code</th> </tr> </thead> <tbody> <tr> <td>AX6601-1.5V</td> <td>LD</td> </tr> <tr> <td>AX6601-1.8V</td> <td>LG</td> </tr> <tr> <td>AX6601-2.5V</td> <td>LQ</td> </tr> <tr> <td>AX6601-2.8V</td> <td>LT</td> </tr> <tr> <td>AX6601-3.0V</td> <td>LW</td> </tr> <tr> <td>AX6601-3.3V</td> <td>La</td> </tr> </tbody> </table>	Part Number	Identification Code	AX6601-1.5V	LD	AX6601-1.8V	LG	AX6601-2.5V	LQ	AX6601-2.8V	LT	AX6601-3.0V	LW	AX6601-3.3V	La
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### ❖ ABSOLUTE MAXIMUM RATINGS (at $T_A=25\text{ }^\circ\text{C}$ )

Characteristics		Symbol	Rating	Unit
VIN Pin Voltage		$V_{IN}$	GND - 0.3 to GND + 6.5	V
Output Voltage		$V_{OUT}$	GND - 0.3 to $V_{IN} + 0.3$	V
Power Dissipation	SOT-23-3L	PD	400	mW
	SOT-89-3L		625	
Storage Temperature Range		$T_{ST}$	-40 to +150	$^\circ\text{C}$
Operating Junction Temperature		$T_J$	-40 to +125	$^\circ\text{C}$
Thermal Resistance from Junction to case	SOT-23-3L	$\theta_{JC}$	180	$^\circ\text{C/W}$
	SOT-89-3L		100	
Thermal Resistance from Junction to ambient	SOT-23-3L	$\theta_{JA}$	250	$^\circ\text{C/W}$
	SOT-89-3L		160	

Note:  $\theta_{JA}$  is measured with the PCB copper area of approximately  $1\text{ in}^2$  (Multi-layer). That need connect to  $V_{IN}$  pin.

### ❖ ELECTRICAL CHARACTERISTICS

$T_A=25\text{ }^\circ\text{C}$  (unless otherwise noted)

Characteristics	Symbol	Conditions	Min	Typ	Max	Units	
Input Voltage	$V_{IN}$	(Note 1)	2.6	-	6	V	
Output Voltage Accuracy (Note2)	$\Delta V_{OUT}$	$V_{IN}=V_{OUT}+0.1\text{V}$ to 6V $V_{OUT} \geq 1.80\text{V}$ , $I_{OUT}=1\text{mA}$	-3	-	+3	%	
		$V_{IN}=2.6\text{V}$ to 6V $1.80 > V_{OUT} \geq 1.20\text{V}$ , $I_{OUT}=1\text{mA}$	-4	-	+4		
Dropout Voltage	$V_{DROP}$	$I_{OUT}=300\text{mA}$	$V_{OUT}=1.50\text{V}$	-	1.10	1.3	V
			$V_{OUT}=1.80\text{V}$	-	0.8	1.0	
			$V_{OUT}=2.50\text{V}$	-	0.4	0.6	
			$V_{OUT}=2.80\text{V}$	-	0.38	0.5	
			$V_{OUT}=3.00\text{V}$	-	0.37	0.5	
			$V_{OUT}=3.30\text{V}$	-	0.36	0.5	
Quiescent Current	$I_Q$	$I_{OUT}=0\text{mA}$ , $V_{IN}=5\text{V}$	-	90	-	$\mu\text{A}$	
Load Regulation (Note 3)	$\Delta V_{LOAD}$	$I_{OUT}=10\text{m}\sim 300\text{mA}$	-	-	0.02	%/mA	
Current Limit	$I_{LIMIT}$	(Note 4)	350	600	-	mA	
Short Circuit Current	$I_{SHORT}$	$V_{OUT}<1.0\text{V}$	-	170	-	mA	
Ripple Rejection	PSRR	$C_{IN}=1\mu\text{F}$ , $C_O=2.2\mu\text{F}$ , $I_{OUT}=10\text{mA}$	$F=120\text{Hz}$	-	60	-	dB
			$F=1\text{KHz}$	-	55	-	
Thermal Shutdown	$T_{SD}$		-	150	-	$^\circ\text{C}$	
Thermal Shutdown Hysteresis	$T_{SH}$		-	30	-	$^\circ\text{C}$	

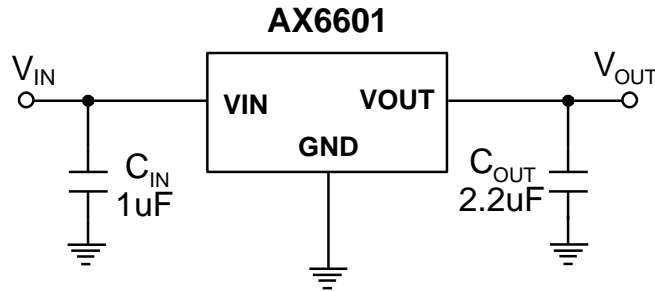
Note1: Minimum  $V_{IN}$  voltage is defined by output adds a dropout voltage.

Note2:  $V_{IN}$  minimum must larger than 2.6V.

Note3: Regulation is measured at constant junction temperature by using pulsed testing with a low ON time.

Note4:  $V_{IN}=V_{OUT}+V_{DROP(\text{Max.})}+0.5\text{V}$ , Current limit is measured at constant junction temperature by using pulsed testing with a low ON time.

## ❖ APPLICATION CIRCUIT



## ❖ FUNCTION DESCRIPTIONS

A minimum of 2.2 $\mu$ F capacitor must be connected from  $V_{OUT}$  to ground to insure stability. Typically a large storage capacitor is connected from  $V_{IN}$  to ground to ensure that the input voltage does not sag below the minimum dropout voltage during the load transient response. This pin must always be dropout voltage higher than  $V_{OUT}$  in order for the device to regulate properly.

## ❖ APPLICATION INFORMATION

Like any low-dropout regulator, the AX6601 requires input and output decoupling capacitors. The device is specifically designed for portable applications requiring minimum board space and smallest components. These capacitors must be correctly selected for good performance (see Capacitor Characteristics Section). Please note that linear regulators with a low dropout voltage have high internal loop gains which require care in guarding against oscillation caused by insufficient decoupling capacitance.

### Capacitor Selection

Normally, use a 1 $\mu$ F capacitor on the input and a 2.2 $\mu$ F capacitor on the output of the AX6601. Larger input capacitor values and lower ESR (X5R, X7R) provide better supply-noise rejection and transient response. A higher- value output capacitor (10 $\mu$ F) may be necessary if large, fast transients are anticipated and the device is located several inches from the power source.

### Input-Output (Dropout) Voltage

A regulator's minimum input-to-output voltage differential (dropout voltage) determines the lowest usable supply voltage. In battery-powered systems, this determines the useful end-of-life battery voltage. Because the device uses a PMOS, its dropout voltage is a function of drain-to source on-resistance,  $R_{DS(ON)}$ , multiplied by the load current:

$$V_{DROPOUT} = V_{IN} - V_{OUT} = R_{DS(ON)} \times I_{OUT}$$

## Current Limit and Short-Circuit Protection

The AX6601 uses a current sense-resistor to monitor the output current. A portion of the PMOS output transistor's current is mirrored to a resistor such that the voltage across this resistor is proportional to the output current. Once the output current exceeds limit threshold, AX6601 would be protected with a limited output current. Further more, when the output is short to ground, the output current would be folded-back to a less limit.

## Thermal Considerations

The AX6601 series can deliver a current of up to 300mA over the full operating junction temperature range. However, the maximum output current must be dated at higher ambient temperature to ensure the junction temperature does not exceed 125°C. With all possible conditions, the junction temperature must be within the range specified under operating conditions. Power dissipation can be calculated based on the output current and the voltage drop across regulator.

$$P_D = (V_{IN} - V_{OUT}) I_{OUT}$$

The final operating junction temperature for any set of conditions can be estimated by the following thermal equation:

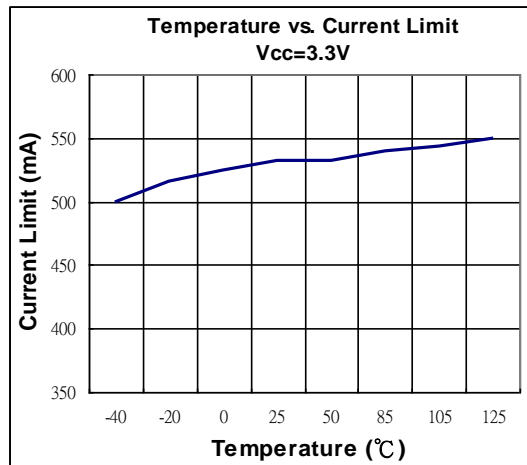
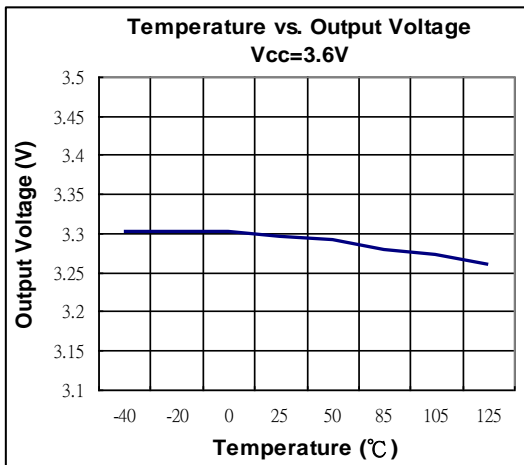
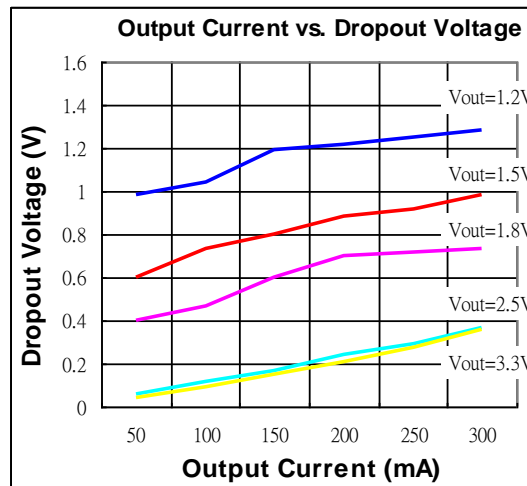
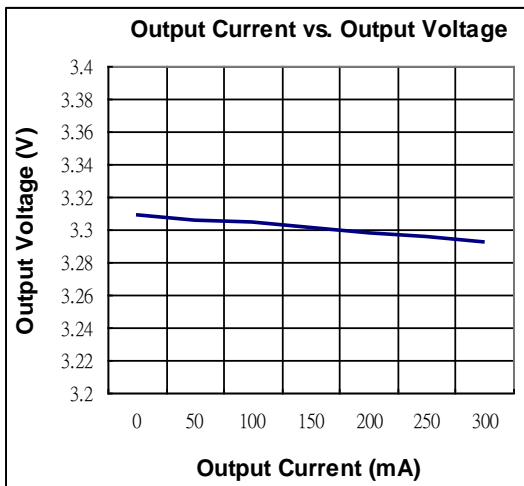
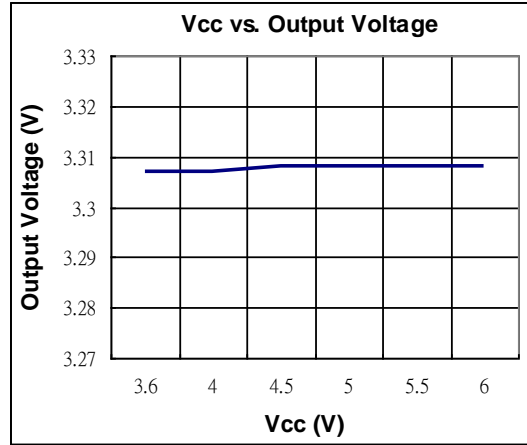
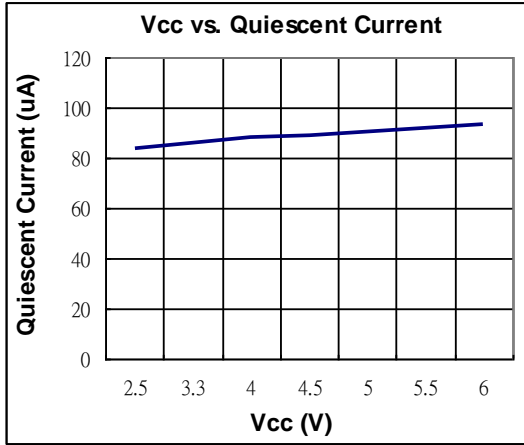
$$P_{D(MAX)} = (T_{J(MAX)} - T_A) / \theta_{JA}$$

Where  $T_{J(MAX)}$  is the maximum junction temperature of the die (125°C) and  $T_A$  is the maximum ambient temperature. The junction to ambient thermal resistance ( $\theta_{JA}$ ) for SOT-23-3L package at recommended minimum footprint is 250°C/W.

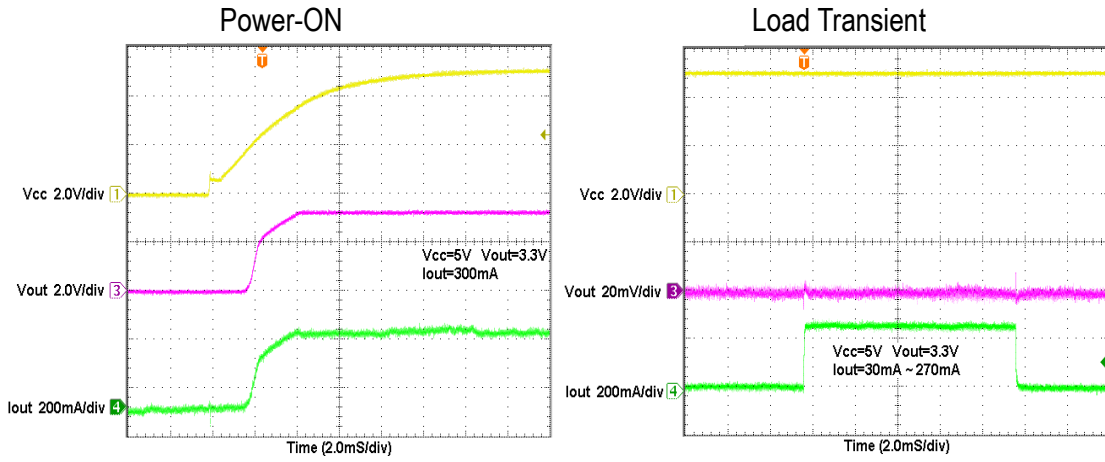
## PCB Layout

An input capacitance of  $\cong 1\mu F$  is required between the AX6601 input pin and ground (the amount of the capacitance may be increased without limit), this capacitor must be located a distance of not more than 1cm from the input and return to a clean analog ground. Input capacitor can filter out the input voltage spike caused by the surge current due to the inductive effect of the package pin and the printed circuit board's routing wire. Otherwise, the actual voltage at the IN pin may exceed the absolute maximum rating. The output capacitor also must be located a distance of not more than 1cm from output to a clean analog ground. Because it can filter out the output spike caused by the surge current due to the inductive effect of the package pin and the printed circuit board's routing wire.

❖ TYPICAL CHARACTERISTICS

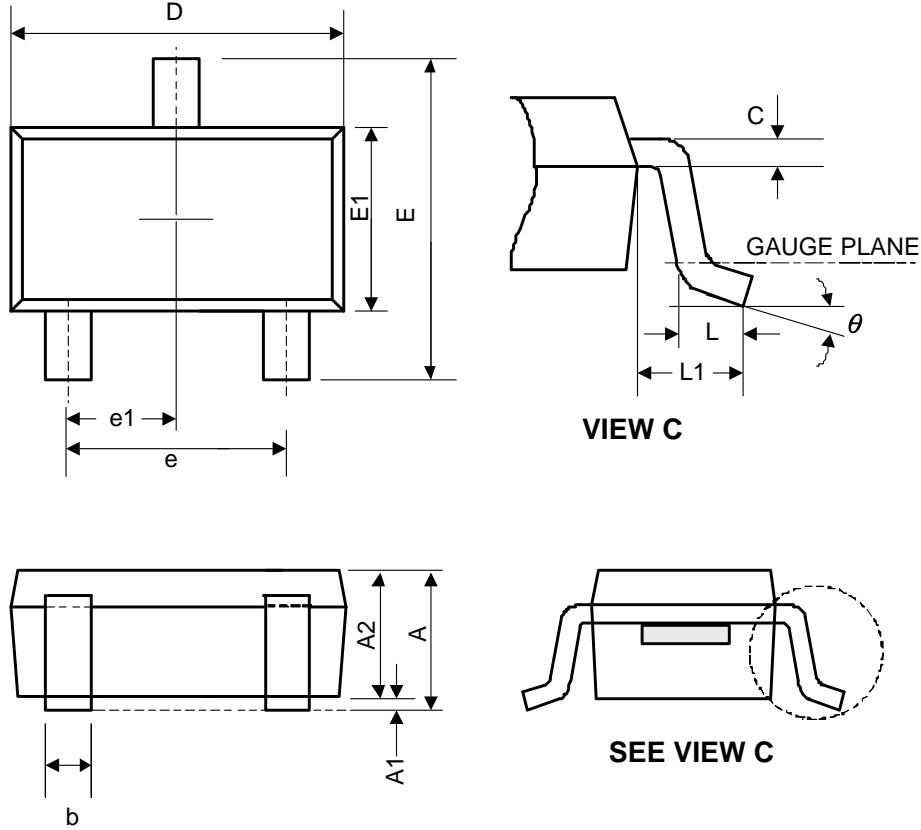


❖ TYPICAL CHARACTERISTICS (CONTINUES)



❖ PACKAGE OUTLINES

(1) SOT-23-3L

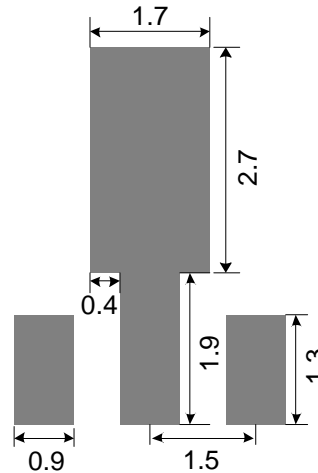
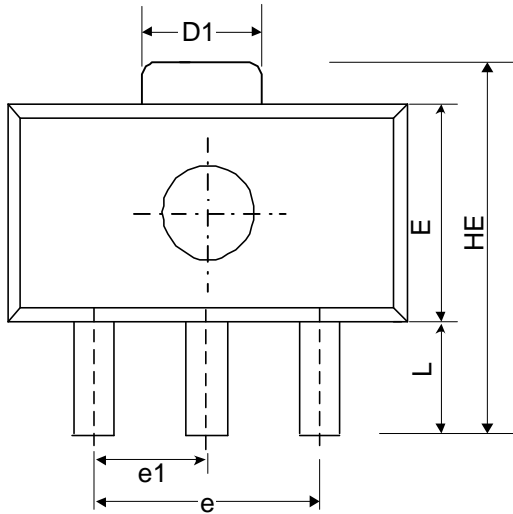


Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	-	-	1.45	-	-	0.057
A1	0	0.08	0.15	-	-	0.006
A2	0.9	1.1	1.3	0.035	0.043	0.051
b	0.3	0.4	0.5	0.012	0.016	0.02
C	0.08	0.15	0.22	0.003	0.006	0.009
D	2.7	2.9	3.1	0.106	0.114	0.122
E	2.6	2.8	3	0.102	0.11	0.118
E1	1.4	1.6	1.8	0.055	0.063	0.071
L	0.3	0.45	0.6	0.012	0.018	0.024
L1	0.5	0.6	0.7	0.02	0.024	0.028
e	1.9 BSC			0.075 BSC		
e1	0.95 BSC			0.037 BSC		
$\theta$	0°	4°	8°	0°	4°	8°

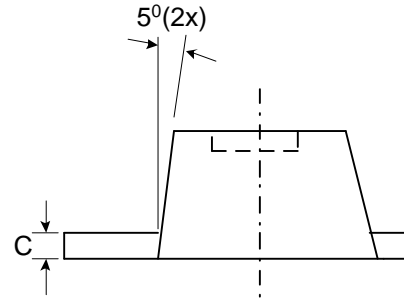
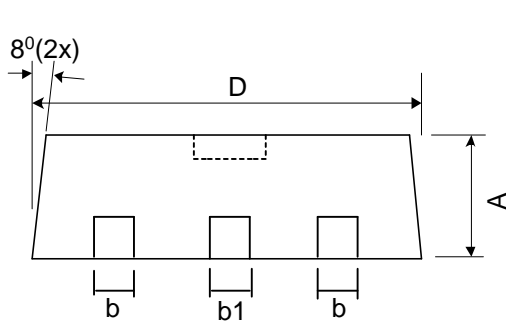
JEDEC outline: NA



(2) SOT-89-3L



Land Pattern Recommendation (Unit: mm)

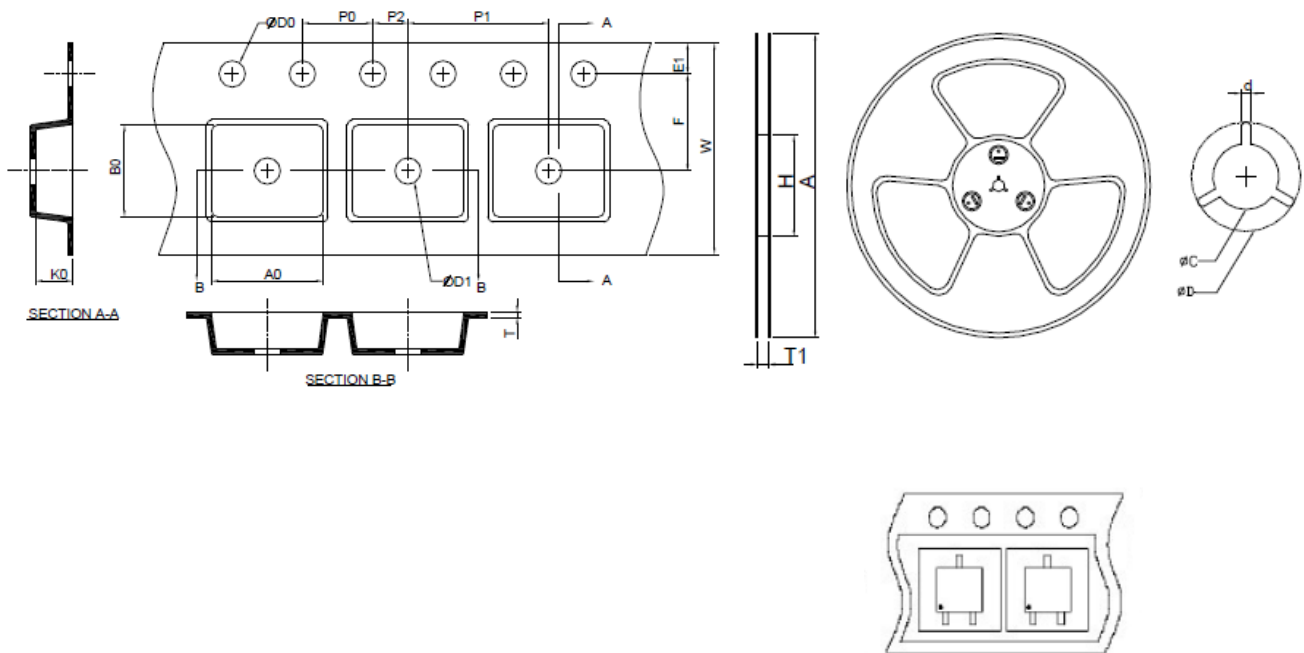


Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.4	1.5	1.6	0.055	0.059	0.063
b	0.36	0.42	0.48	0.014	0.017	0.019
b1	0.44	0.5	0.56	0.017	0.02	0.022
C	0.35	0.4	0.44	0.014	0.016	0.017
D	4.4	4.5	4.6	0.173	0.177	0.181
D1	1.35	1.59	1.83	0.053	0.063	0.072
e	3.0 BSC			0.118 BSC		
e1	1.5 BSC			0.059 BSC		
E	2.29	2.45	2.6	0.09	0.097	0.102
HE	3.94	4.1	4.25	0.155	0.161	0.167
L	0.8	1	1.2	0.031	0.04	0.047

JEDEC outline: TO-243 AB

❖ Carrier tape dimension

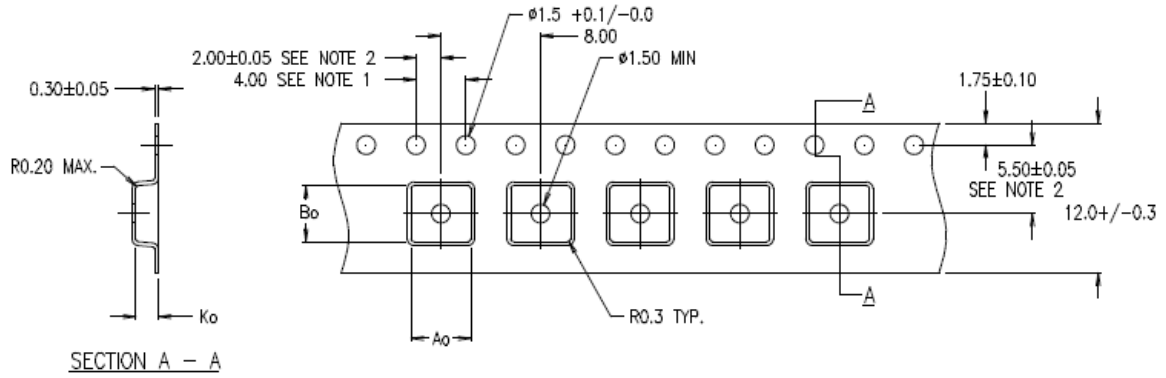
SOT-23-3L



A	H	T1	C	d	D	W	E1	F
178.0±2.00	50 MIN.	8.4+2.00 -0.00	13.0+0.50 -0.20	1.5 MIN.	20.2 MIN.	8.0±0.30	1.75±0.10	3.5±0.05
P0	P1	P2	D0	D1	T	A0	B0	K0
4.0±0.10	4.0±0.10	2.0±0.05	1.5+0.10 -0.00	1.0 MIN.	0.6+0.00 -0.40	3.20±0.20	3.10±0.20	1.50±0.20

(mm)

SOT89-3L



Ao = 4.80  
Bo = 4.50  
Ko = 1.80

