

PWM Control 2A Step-Down Converter

❖ GENERAL DESCRIPTION

AX3111A consists of step-down switching regulator with PWM control. These devise include a reference voltage source, oscillation circuit, error amplifier, internal PMOS and etc.

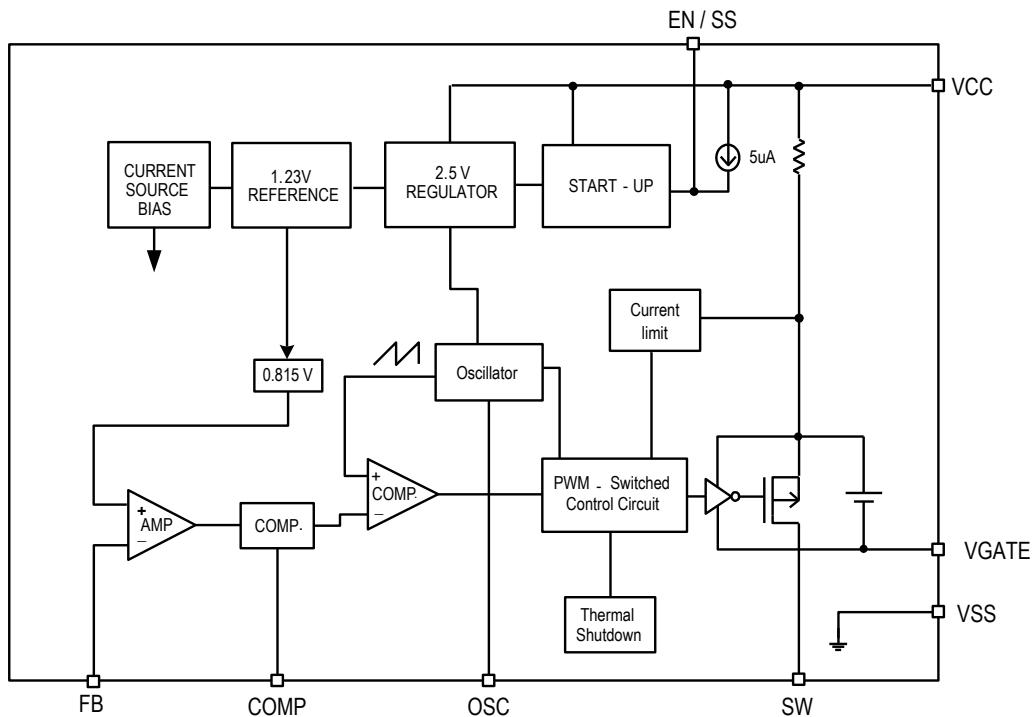
AX3111A provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to the duty ratio linearly form 0 up to 100%. This converter is build out soft start function that prevents overshoot and inrush current at startup. An over current protect function and short circuit protect function are built inside, and when OCP or SCP happens, the operation frequency will be reduced. The operating frequency is decided by outside resistance. An external compensation is easily to system stable; the low ESR output capacitor can be used.

With the addition of an internal P-channel Power MOS, a coil, capacitors, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the SOP-8L packages, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 40V, it is also suitable for the operation via an AC adapter.

❖ FEATURES

- Input voltage : 8V to 40V
- Output voltage : V_{FB} to 38V
- Duty ratio : 0% to 100% PWM control
- Oscillation frequency range is 50K~350KHz by outside resistance setting
- Enable with Soft-Start function
- Current Limit, Short Circuit Protect (SCP) and Thermal Shutdown protection
- Built-in internal SW P-channel MOS.
- SOP-8L-EP Pb-Free packages.
- RoHS and Halogen free compliance.

❖ BLOCK DIAGRAM



❖ PIN ASSIGNMENT

The packages of AX3111A is SOP-8L-EP; the pin assignment is given by:

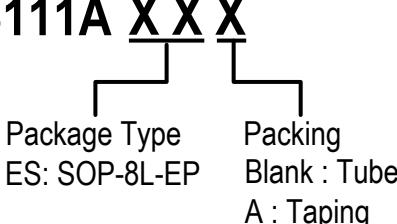
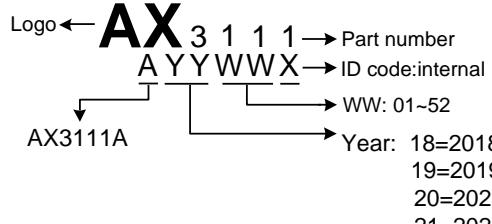
(Top View)

| | | | |
|-------|---|------|---|
| EN/SS | 1 | OSC | 8 |
| VSS | 2 | COMP | 7 |
| VGATE | 3 | FB | 6 |
| VCC | 4 | SW | 5 |

SOP-8L-EP

| Name | Description |
|--------------|---|
| EN/SS | ON/OFF Shutdown and Soft-start pin |
| VSS | GND pin |
| VGATE | Driver GATE clamping pin. The pin must connect a 1uF capacitor to VCC |
| VCC | IC power supply pin |
| SW | Switch pin. Connect external inductor & diode here. |
| FB | Feedback pin |
| COMP | Compensation pin |
| OSC | Frequency Set Pin. The pin connect a resistance to GND. |

❖ ORDER/MARKING INFORMATION

| Order Information | Top Marking (SOP-8L-EP) |
|--|---|
| AX3111A XX X  Package Type ES: SOP-8L-EP Packing Blank : Tube A : Taping |  Logo AX 3 1 1 1 → Part number A Y Y W W X → ID code:internal AX3111A WW: 01~52 Year: 18=2018 19=2019 20=2020 21=2021 22=2022 . 45=2045 |

❖ ABSOLUTE MAXIMUM RATINGS (at $T_A=25^\circ\text{C}$)

| Characteristics | Symbol | Rating | Unit |
|---|---------------|----------------------------------|---------------------------|
| VCC Pin Voltage | V_{CC} | $V_{SS} - 0.3$ to $V_{SS} + 45$ | V |
| Feedback Pin Voltage | V_{FB} | $V_{SS} - 0.3$ to 6 | V |
| EN/SS Pin Voltage | $V_{EN/SS}$ | $V_{SS} - 0.3$ to 6 | V |
| OSC Pin Voltage | V_{OSC} | $V_{SS} - 0.3$ to 3 | V |
| COMP Pin Voltage | V_{COMP} | $V_{SS} - 0.3$ to 6 | V |
| VGATE Pin Voltage | V_{GATE} | $V_{SS} - 0.3$ to V_{CC} | V |
| Switch Pin Voltage | V_{SW} | $V_{SS} - 0.3$ to $V_{CC} + 0.3$ | V |
| Power Dissipation | PD | Internally limited | mW |
| Storage Temperature Range | T_{ST} | -65 to +150 | $^\circ\text{C}$ |
| Operating Junction Temperature Range | T_{OJP} | -40 to +125 | $^\circ\text{C}$ |
| Operating Supply Voltage | V_{OP} | 8 to 40 | V |
| Thermal Resistance from Junction to case | θ_{JC} | 15 | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance from Junction to ambient | θ_{JA} | 40 | $^\circ\text{C}/\text{W}$ |

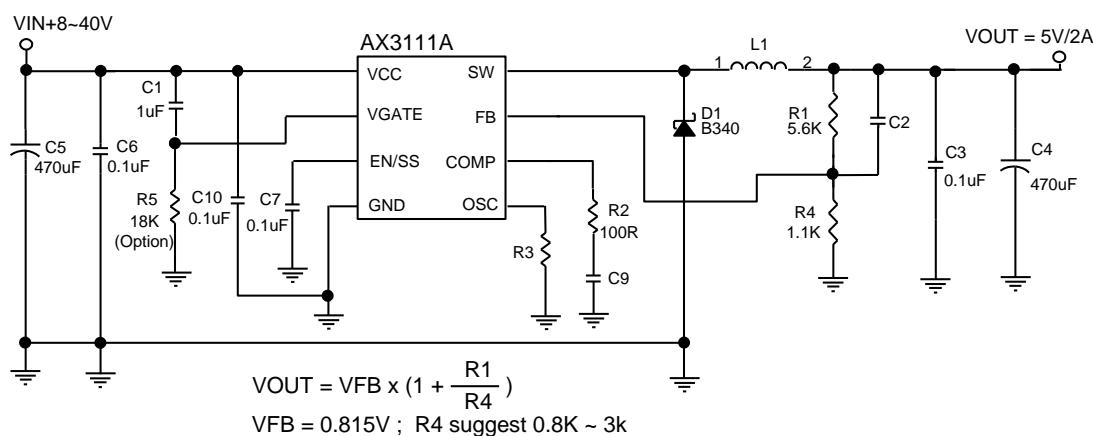
Note: θ_{JA} is measured with the PCB copper area (need connect to Exposed Pad) of approximately 1.5 in² (Multi-layer).

❖ ELECTRICAL CHARACTERISTICS

($V_{CC} = 12V$, $T_A=25^\circ C$, unless otherwise specified)

| Characteristics | Symbol | Conditions | Min | Typ | Max | Units |
|--|--------------|--|-------|-------|-------|-----------|
| Feedback Voltage | V_{FB} | $V_{CC} = 10V\sim30V$ $I_{OUT} = 0$ to $1A$ $T_j = -20^\circ C \sim 125^\circ C$ | 0.800 | 0.815 | 0.830 | V |
| Quiescent Current | I_{CCQ} | $V_{FB}=1.2V$ force driver off | - | 3 | 6 | mA |
| Feedback Bias Current | I_{FB} | $I_{OUT}=0.1A$ | - | 0.1 | 0.5 | uA |
| Shutdown Supply Current | I_{SD} | $V_{EN/SS} = 0V$ | - | 3 | 6 | mA |
| Current Limit | I_{CL} | | 2.5 | - | - | A |
| Adjustable frequency range | F_{OSC} | | 50 | - | 380 | KHz |
| Short frequency | F_{OSC1} | $V_{CC} = 10V\sim30V$ | 45 | 55 | 65 | KHz |
| EN/SS Pin Shutdown Logic input threshold voltage | V_{ENL} | | - | - | 0.8 | V |
| EN/SS Pull high Current | $I_{EN/SS}$ | $V_{EN/SS}=0V$ | - | 5 | - | uA |
| Internal MOSFET $R_{DS(ON)}$ | $R_{DS(ON)}$ | $V_{CC}=12V$, $V_{FB}=0V$ | - | 130 | 180 | $m\Omega$ |
| Efficiency | EFFI | $V_{CC} = 12V$, $V_{OUT} = 5V$, $I_{OUT} = 1A$ | | 90 | | % |
| | | $V_{CC} = 30V$, $V_{OUT} = 5V$, $I_{OUT} = 1A$ | - | 86 | | |

❖ APPLICATION CIRCUIT



| Compensation Table | | | | | | |
|--------------------|----------------|-----------|------|-----|-------|--|
| COUT ESR Range | Frequency (Hz) | VIN Range | L1 | C9 | C2 | |
| 30m~80mΩ | 50K | 8~40V | 100u | 10n | 2700P | |
| | 150K | | 68u | 10n | 2700P | |
| | 250K | | 33u | 10n | 1500P | |
| | 350K | | 22u | 10n | 1800P | |
| 80m~300mΩ | 50K | 8~40V | 100u | 47n | 820P | |
| | 150K | | 68u | 47n | 1200P | |
| | 250K | | 33u | 47n | 1500P | |
| | 350K | | 22u | 47n | 1800P | |

❖ FUNCTION DESCRIPTIONS

EN/SS

This pin can be supplied shutdown or soft start function. It is inside pull high function. For normal application, the pin must be connected a capacitor to ground. There is a 5uA current to charge this capacitor, vary the different capacitor value to control soft start time. Allow the switching regulator circuit to be shutdown pulling this pin below a 0.8V threshold voltage.

OSC

External frequency set pin. The pin connects a resistance (R3) to reduce system frequency. This converter's frequency can be set from 50K to 350KHz, please refer the below table to set frequency.

| T=Room Temperature | | | | |
|--------------------|-----|------|------|------|
| R3 (Ω) | 10M | 240K | 110K | 68K |
| Frequency (Hz) | 52K | 150K | 250K | 350K |

COMP

Compensation pin. For EL output capacitor application, the COMP pin connects R2 and C9 to ground for all condition; please refer the compensation table.

❖ APPLICATION INFORMATION

Setting the Output Voltage

Application circuit item shows the basic application circuit with adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.815V \times \left(1 + \frac{R1}{R4}\right)$$

Table 1 Resistor select for output voltage setting

| V _{OUT} | R4 | R1 |
|------------------|------|------|
| 5V | 1.1K | 5.6K |
| 3.3V | 2.7K | 8.2K |

Inductor Selection

For most designs, the different frequency can be reducing the inductor value; The AX3111A is suggested 22μH to 100μH for 350K to 50KHz frequencies. Please refer the below table to design.

| L1 recommend value (V _{IN} =8~40V, V _{OUT} =5V, I _{OUT} =2A) | | | | |
|---|-------|------|------|------|
| Frequency (Hz) | 50K | 150K | 250K | 350K |
| L1 Value (H) | 100uH | 68uH | 33uH | 22uH |

Where is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 20% of the maximum load current 2A, ΔI_L=0.4A. The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (2A+0.2A).

Input Capacitor Selection

This capacitor should be located close to the IC using short leads and the voltage rating should be approximately 1.5 times the maximum input voltage. The RMS current rating requirement for the input capacitor of a buck regulator is approximately 1/2 the DC load current. A low ESR input capacitor sized for maximum RMS current must be used. A 470μF low ESR capacitor for most applications is sufficient.

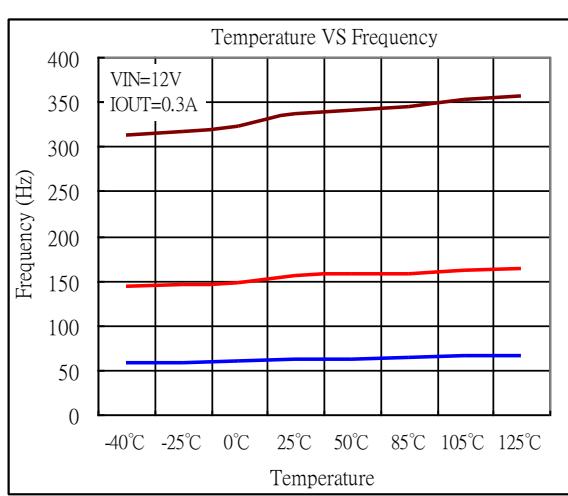
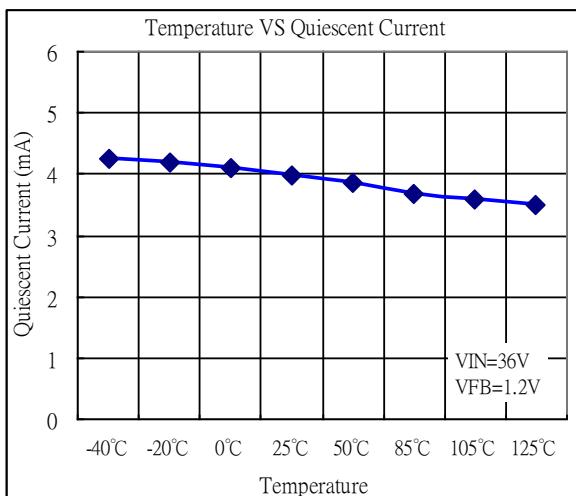
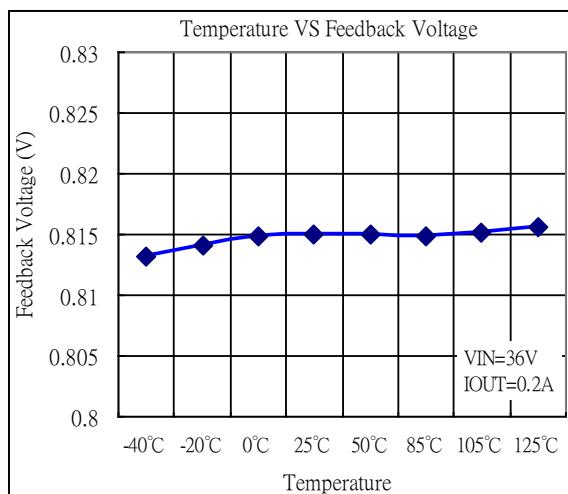
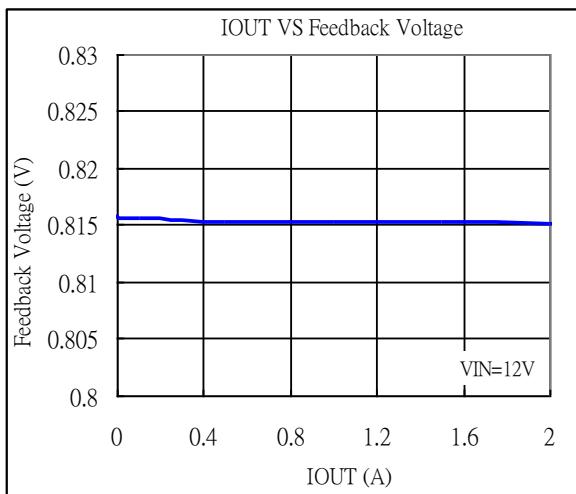
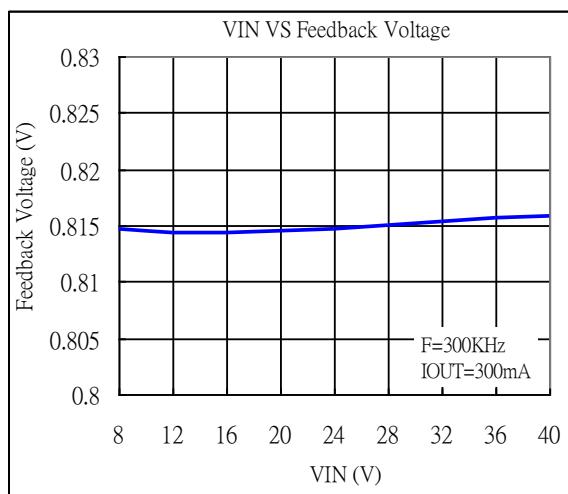
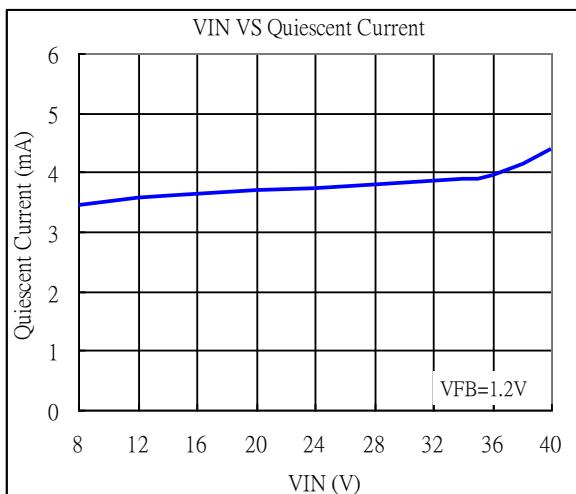
Output Capacitor Selection

The output capacitor is required to filter the output and provide regulator loop stability. The important capacitor parameters are; the 100 KHz Equivalent Series Resistance (ESR), the RMS ripples current rating, voltage rating, and capacitance value. For the output capacitor, the ESR value is the most important parameter. The ESR can be calculated from the following formula.

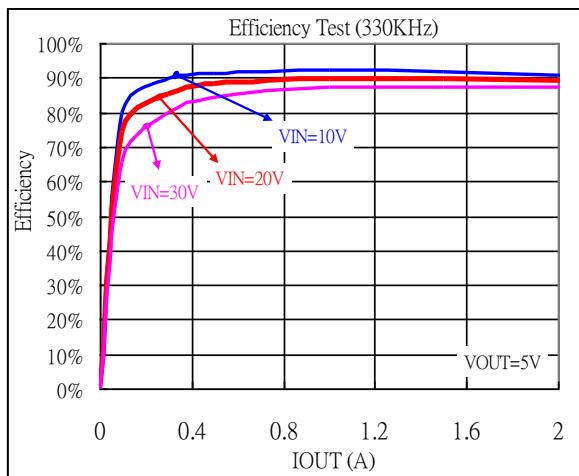
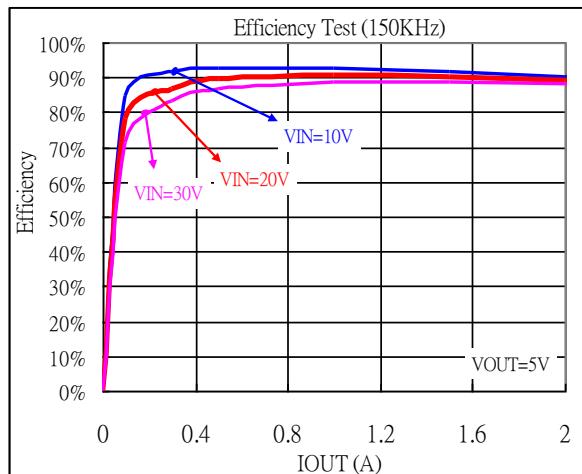
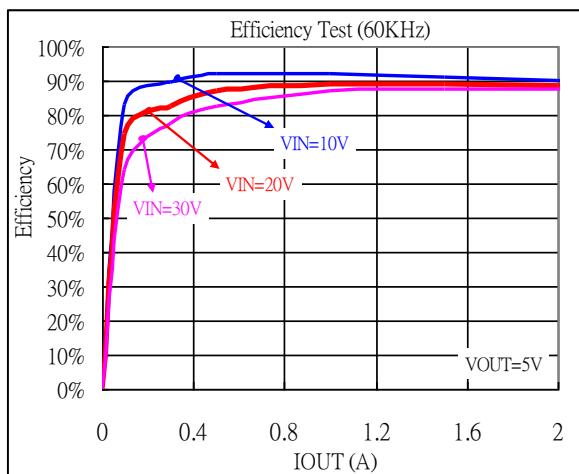
$$V_{RIPPLE} = \Delta I_L \times ESR = 0.4A \times 80m\Omega = 32mV$$

An aluminum electrolytic capacitor's ESR value is related to the capacitance and its voltage rating. In most case, higher voltage electrolytic capacitors have lower ESR values. Most of the time, capacitors with much higher voltage ratings may be needed to provide the low ESR values required for low output ripple voltage. It is recommended to replace this low ESR capacitor by using a $470\mu F$ low ESR values $< 80m\Omega$.

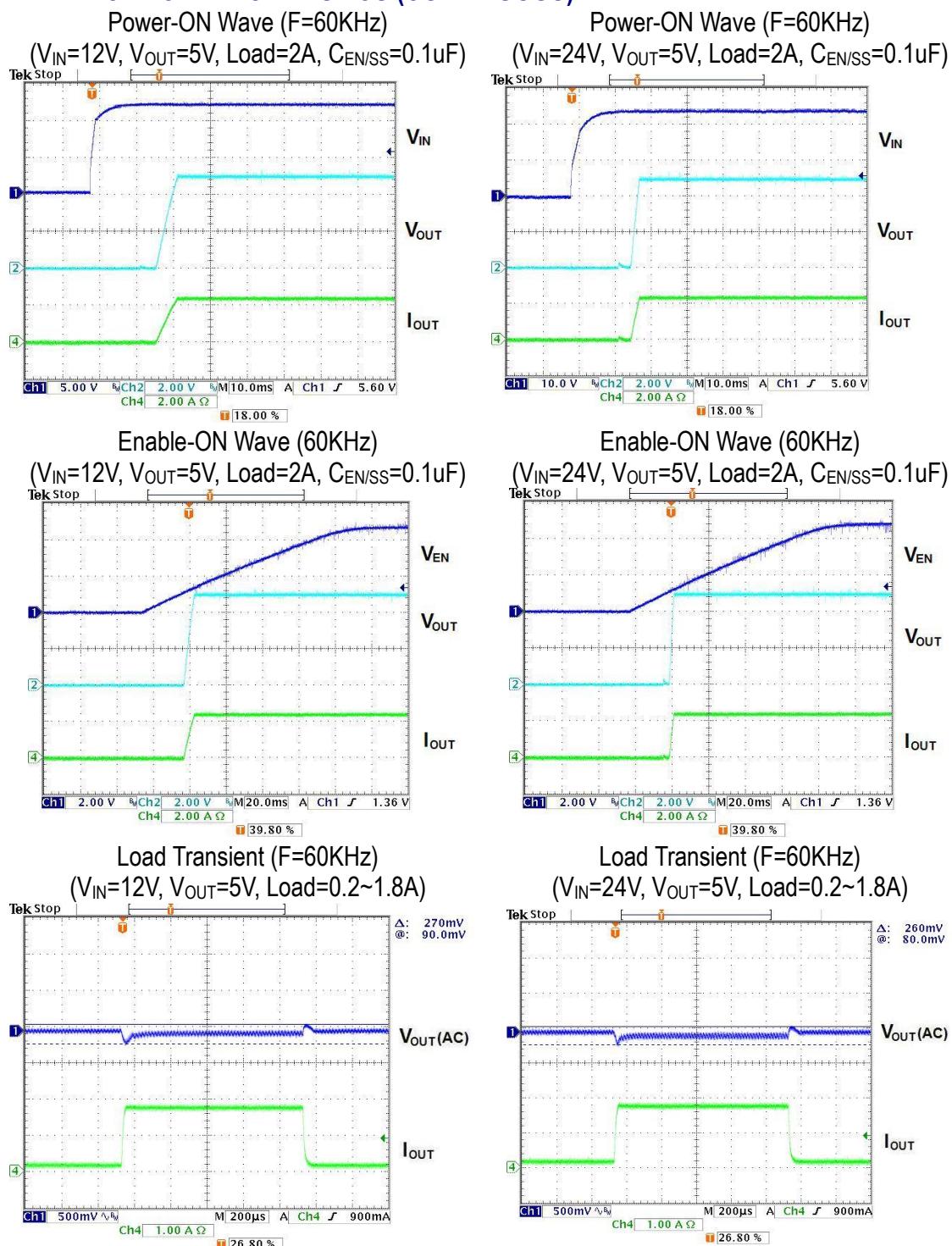
❖ TYPICAL CHARACTERISTICS



❖ TYPICAL CHARACTERISTICS (CONTINUOUS)

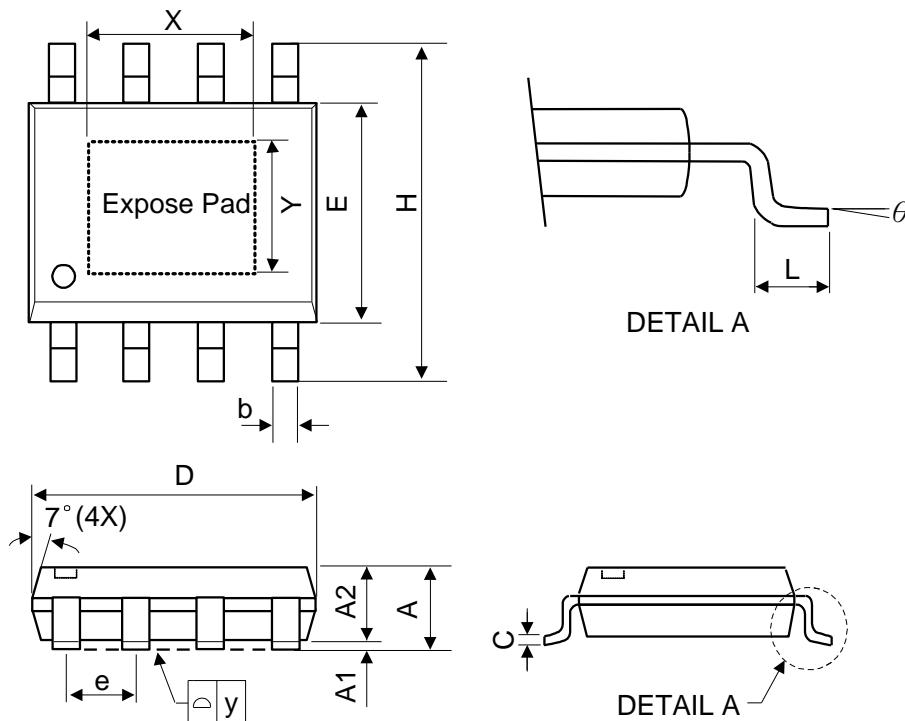


❖ TYPICAL CHARACTERISTICS (CONTINUOUS)



❖ PACKAGE OUTLINES

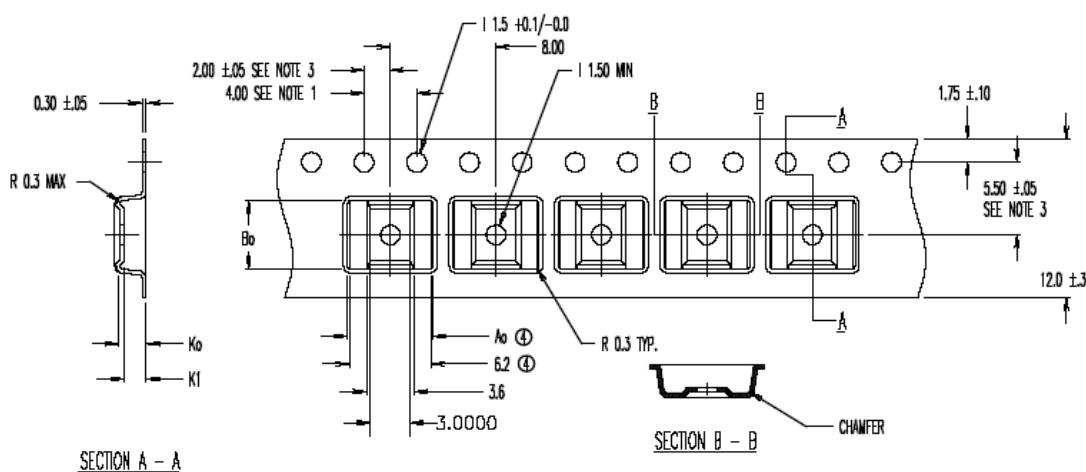
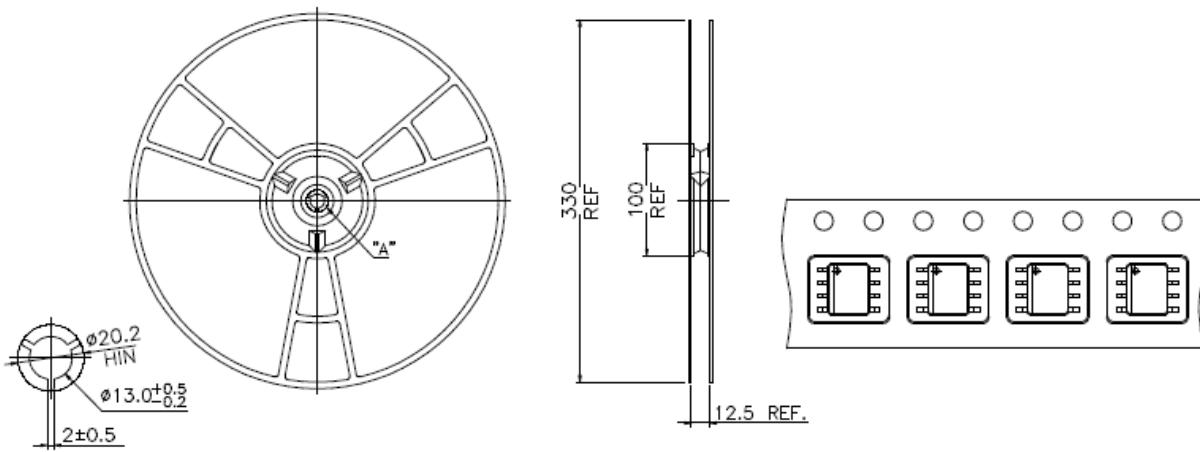
SOP-8L-EP



| Symbol | Dimensions in Millimeters | | | Dimensions in Inches | | |
|--------|---------------------------|------|------|----------------------|-------|-------|
| | Min. | Nom. | Max. | Min. | Nom. | Max. |
| A | - | - | 1.75 | - | - | 0.069 |
| A1 | 0 | - | 0.15 | 0 | - | 0.06 |
| A2 | 1.25 | - | - | 0.049 | - | - |
| C | 0.1 | 0.2 | 0.25 | 0.0075 | 0.008 | 0.01 |
| D | 4.7 | 4.9 | 5.1 | 0.185 | 0.193 | 0.2 |
| E | 3.7 | 3.9 | 4.1 | 0.146 | 0.154 | 0.161 |
| H | 5.8 | 6 | 6.2 | 0.228 | 0.236 | 0.244 |
| L | 0.4 | - | 1.27 | 0.015 | - | 0.05 |
| b | 0.31 | 0.41 | 0.51 | 0.012 | 0.016 | 0.02 |
| e | 1.27 BSC | | | 0.050 BSC | | |
| y | - | - | 0.1 | - | - | 0.004 |
| X | - | 2.34 | - | - | 0.092 | - |
| Y | - | 2.34 | - | - | 0.092 | - |
| θ | 0° | - | 8° | 0° | - | 8° |

Mold flash shall not exceed 0.25mm per side

JEDEC outline: MS-012 BA

❖ Carrier tape dimension
SOP-8L-EP

Notes:

- ④ $A_0 = 6.50$
- ④ $B_0 = 5.20$
- ④ $K_0 = 2.10$
- ④ $K_1 = 1.70$
- 1. 10 sprocket hole pitch cumulative tolerance $\pm 0.2\text{mm}$
- 2. Camber not to exceed 1mm in 100mm.
- 3. Material: Anti-Static Black Advantek Polystyrene.
- 4. A_0 and B_0 measured on a plane 0.3mm above the bottom of the pocket.
- 5. K_0 measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
- 6. Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.