

2A Ultra Low Dropout Linear Regulator

❖ GENERAL DESCRIPTION

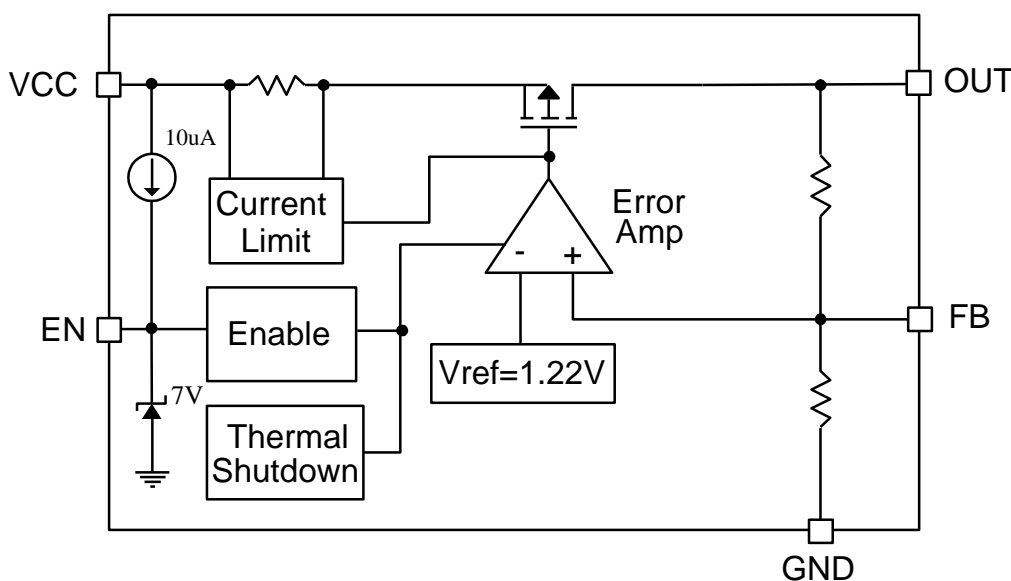
The AX1205 is a low-dropout voltage regulator suitable for various electronic equipments. It provides constant voltage power source. The dropout voltage of AX1205 is below 0.3V in full rated current (2A). This regulator has various functions such as a peak current protection, a thermal shut down, a short circuit protect.

The AX1205 is available in SOP-8L power package which features small size to reduce the junction-to-case resistance, being applicable in 0.1~1.6W applications.

❖ FEATURES

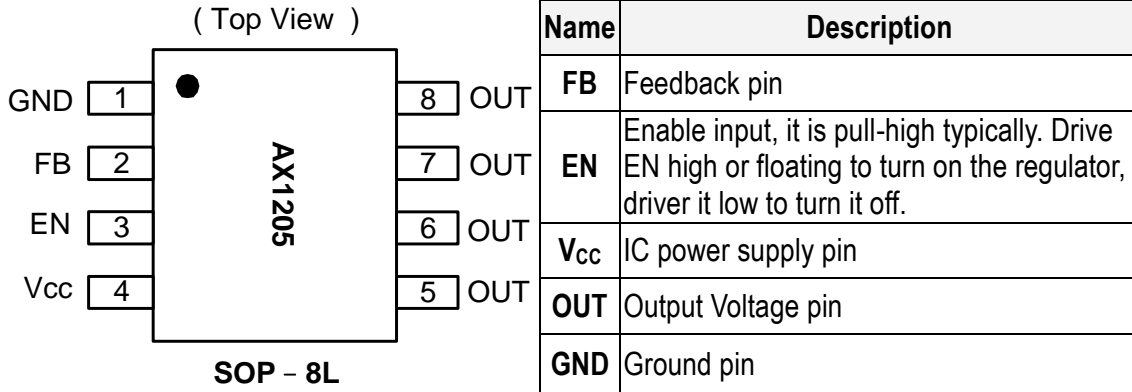
- Ultra Low Dropout - 0.3V(typical) at 2A Output Current
- Adjustable mode: 1.22V Reference Voltage
- Operating voltage can be up to 12V.
- Current-Limit and Thermal Shutdown Protection
- Short circuit protection, Enable function.
- Built-in internal SW P-channel MOS
- SOP-8L Pb-Free Package.
- RoHS and Halogen free compliance.

❖ BLOCK DIAGRAM



❖ PIN ASSIGNMENT

The package of AX1205 is SOP-8L; the pin assignment is given by:


❖ ORDER/MARKING INFORMATION

Order Information
AX1205 X X ↙ ↘ Package Packing S : SOP-8L Blank : Tube A : Taping
Top Marking
ADJ Version <div style="text-align: center;"> </div>

❖ ABSOLUTE MAXIMUM RATINGS (T_A=25°C)

Characteristics	Symbol	Rating	Unit
V _{CC} Supply Voltage	V _{CC}	-0.3 to 16	V
EN Pin Voltage	V _{EN}	-0.3 to 7	V
FB Pin Voltage	V _{FB}	-0.3 to V _{CC} +0.3	V
Output current	I _o	2.5	A
Power Dissipation	PD	1.6	W
Storage Temperature Range	T _{ST}	-65 to +150	°C
Junction Temperature Range	T _J	-40 to 125	°C
Operating Temperature Range	T _{OP}	-40 to +85	°C

Thermal Resistance from Junction to case	θ_{JC}	20	°C/W
Thermal Resistance from Junction to ambient	θ_{JA}	60	°C/W

Note: θ_{JA} is measured with the PCB copper area(need connect to OUT pin) of approximately 1.5 in² (Multi-layer).

❖ ELECTRICAL CHARACTERISTICS

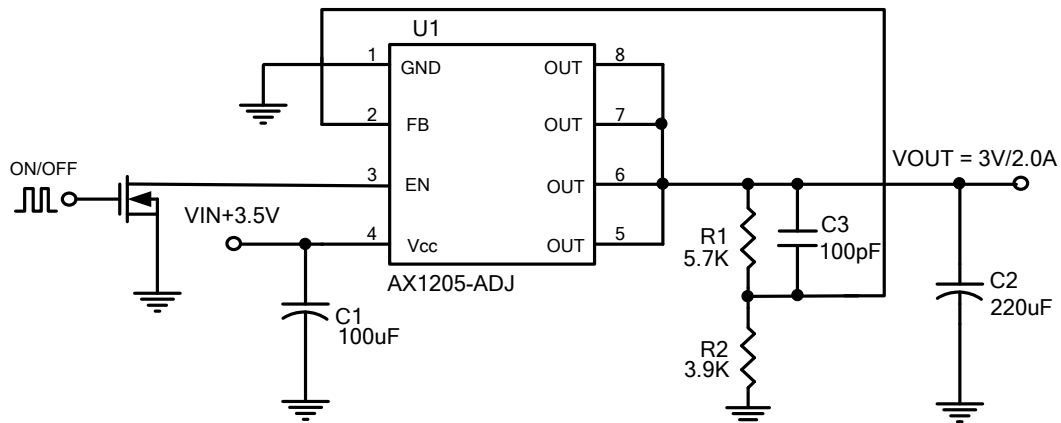
(Unless otherwise specified, $T_A=25^{\circ}\text{C}$, $V_{CC}=5\text{V}$)

Characteristics	Symbol	Conditions	Min	Typ	Max	Units
V_{CC} Supply Voltage	V_{CC}	$I_{OUT}=2\text{A}$	3.1	-	12	V
Feedback Voltage	V_{FB}	$I_{OUT}=10\text{mA}$, $V_{CC}=5.0\text{V}$	1.196	1.22	1.244	V
Output Voltage	V_{OUT}	$I_{OUT}=10\text{mA}$, $V_{CC}=3.2\text{V}$	2.45	2.5	2.55	V
		$I_{OUT}=10\text{mA}$, $V_{CC}=4.0\text{V}$	3.234	3.3	3.366	
		$I_{OUT}=10\text{mA}$, $V_{CC}=5.5\text{V}$	4.90	5.0	5.10	
GND Current	I_{GND}	$I_{OUT}=0\sim 2\text{A}$	-	1.2	3	mA
Shutdown Current	I_{SD}	$V_{EN}=0\text{V}$	-	0.1	0.4	mA
Load regulation	V_{Load}	$5\text{mA} < I_{OUT} < 2\text{A}$	-	0.5	1.5	%
Line regulation	V_{Line}	$I_{OUT}=10\text{mA}$, $V_{OUT}+0.5\text{V} < V_{CC} < 12\text{V}$	-	0.1	0.5	%
Ripple rejection ratio	PSRR	Note1	-	65	-	dB
Dropout Voltage	V_{DROP}	$I_{OUT}=2\text{A}$, $V_{OUT}=3.3\text{V}$	-	0.3	0.4	V
		$I_{OUT}=2\text{A}$, $V_{OUT}=5\text{V}$	-	0.2	0.3	
Short circuit protect	I_{scp}	$V_{OUT} < 20\%$	-	0.6	-	A
Current Limit	CL		2.2	-	-	A
EN Pin Logic input threshold voltage	V_{ENH}	High (regulator ON)	2.0	-	-	V
	V_{ENL}	Low (regulator OFF)	-	-	0.8	V
EN Pin Input Current	I_{ENH}	$V_{EN}=2.5\text{V}$ (ON)	-	20	-	μA
	I_{ENL}	$V_{EN}=0.3\text{V}$ (OFF)	-	-10	-	μA
Internal MOSFET RDSON	R_{DSON}	$V_{CC}=3.5\text{V}$,	-	150	180	m Ω
		$V_{CC}=5\text{V}$,	-	100	120	
Thermal Shutdown	TSD		-	140	-	°C

Note: These parameters, although guaranteed, are not 100% tested in production.

❖ APPLICATION CIRCUIT

ADJ



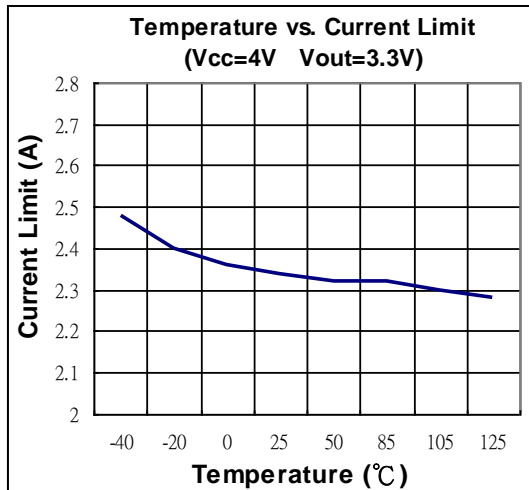
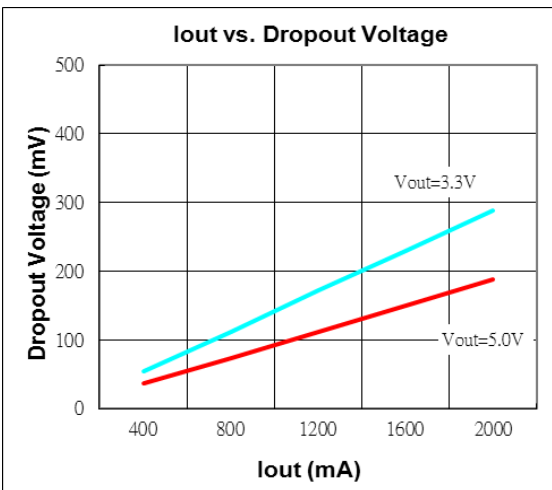
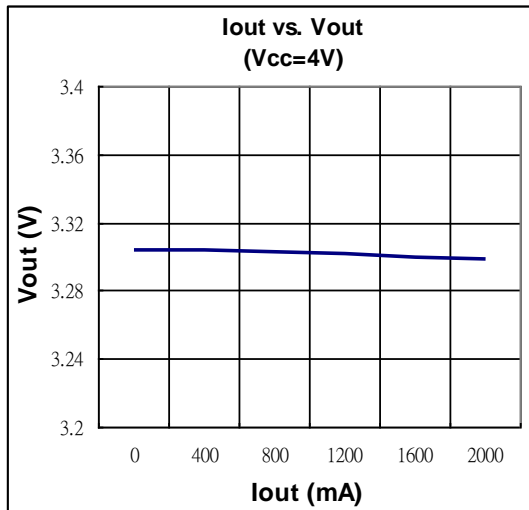
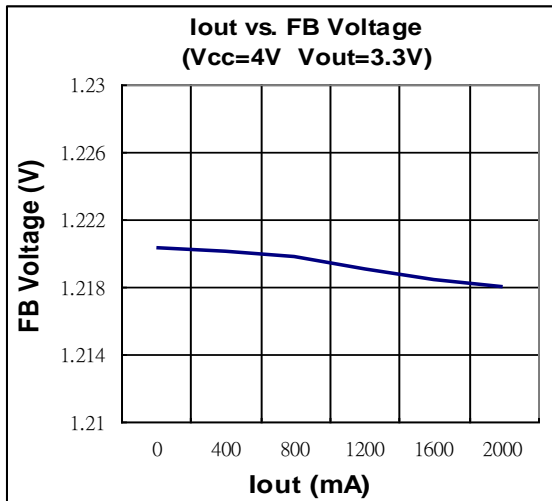
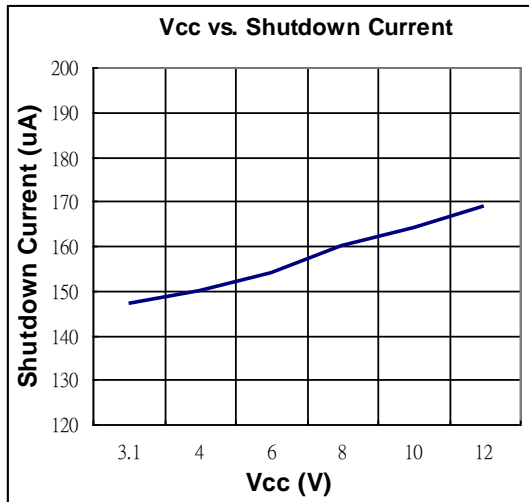
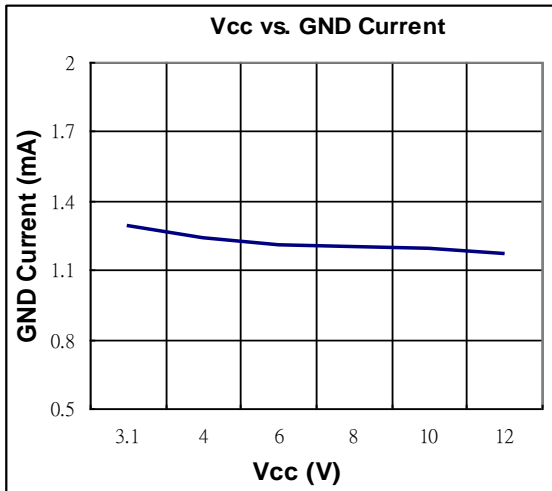
$$V_{OUT} = V_{FB} * (1 + R1/R2)$$

$$V_{FB} = 1.22V$$

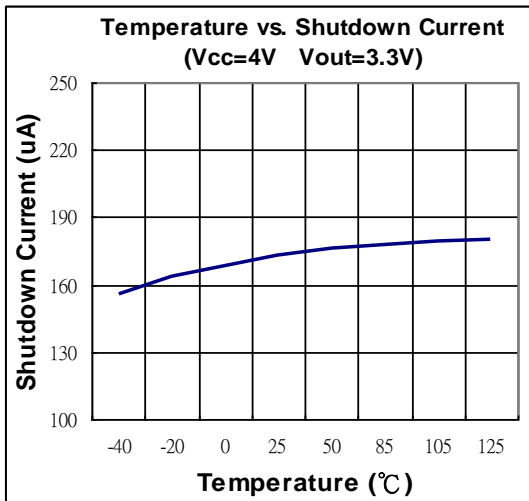
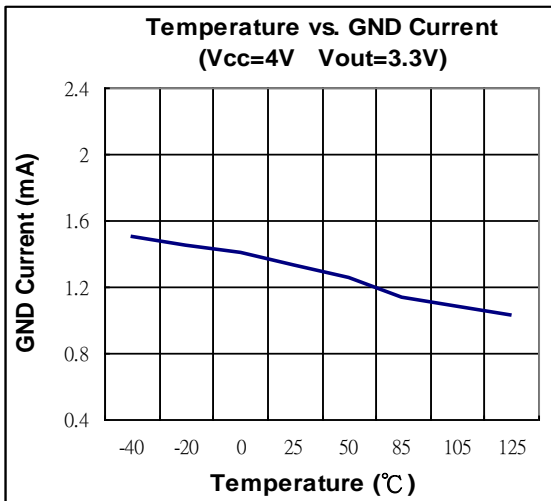
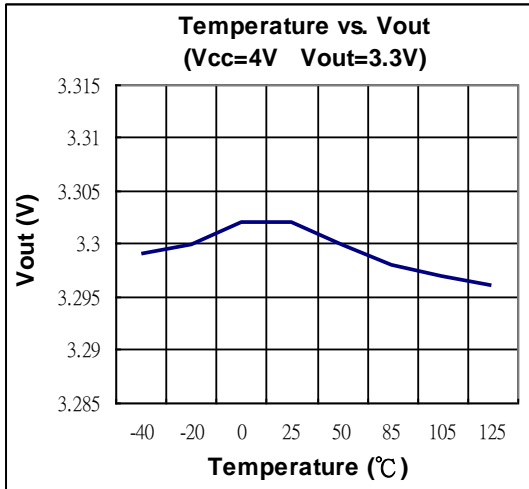
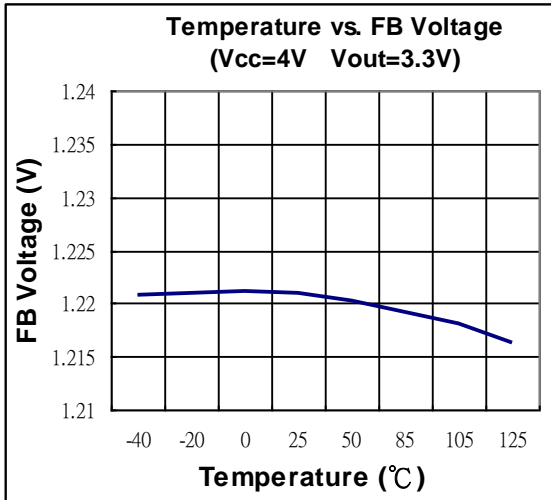
R2 suggest 1K~5.6KΩ

C2 choose Low ESR capacitor

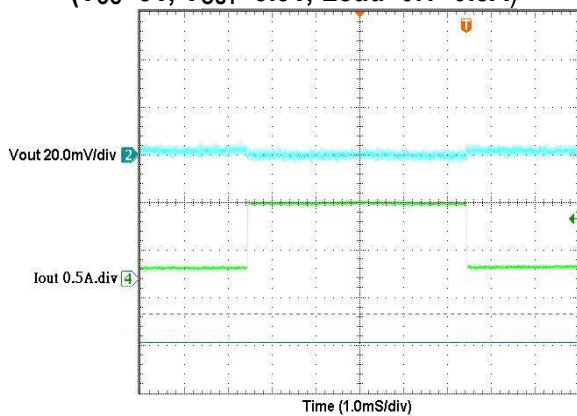
C3=47pF~100pF for stability issue

❖ TYPICAL CHARACTERISTICS


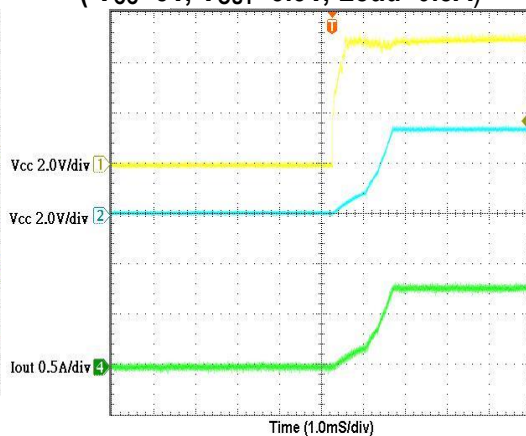
❖ TYPICAL CHARACTERISTICS (CONTINUED)



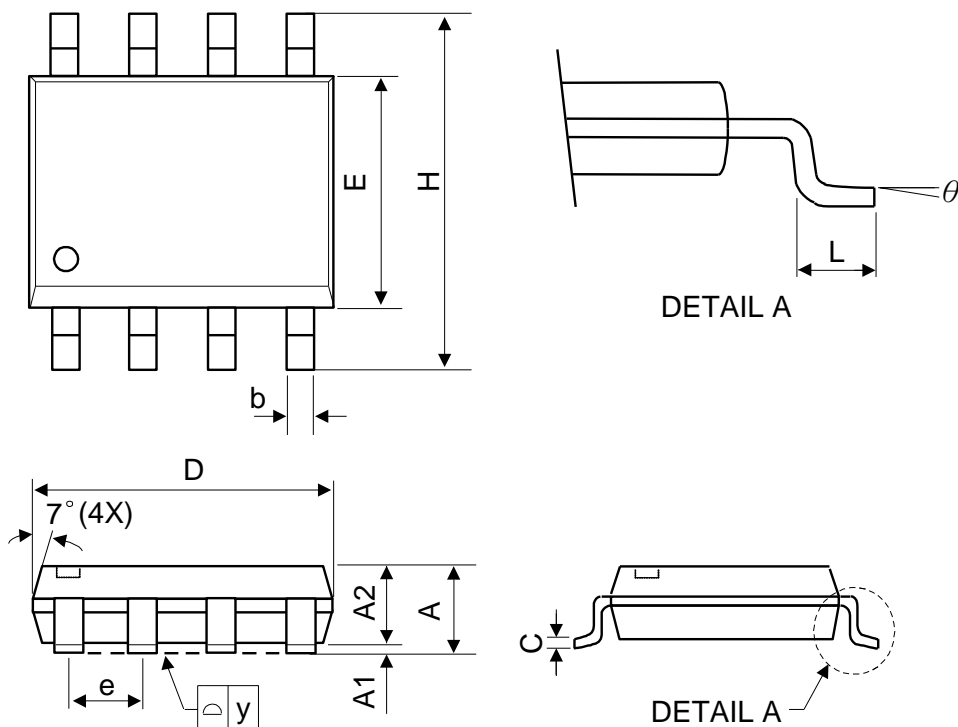
Load Transient
(V_{CC}=5V, V_{OUT}=3.3V, Load=0.1~0.8A)



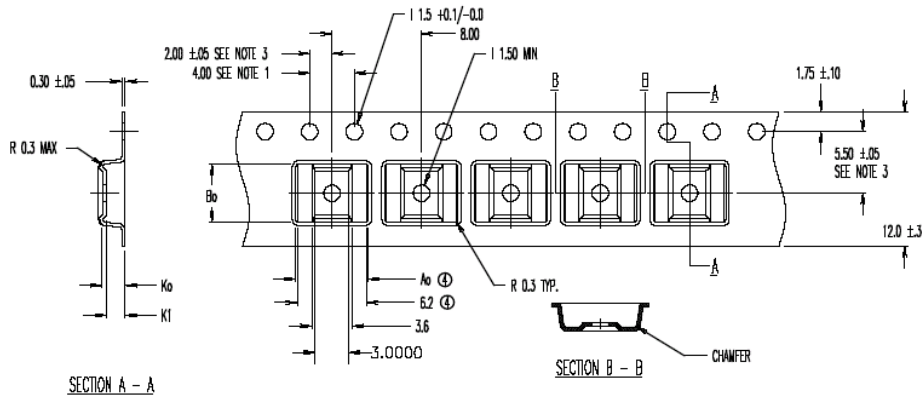
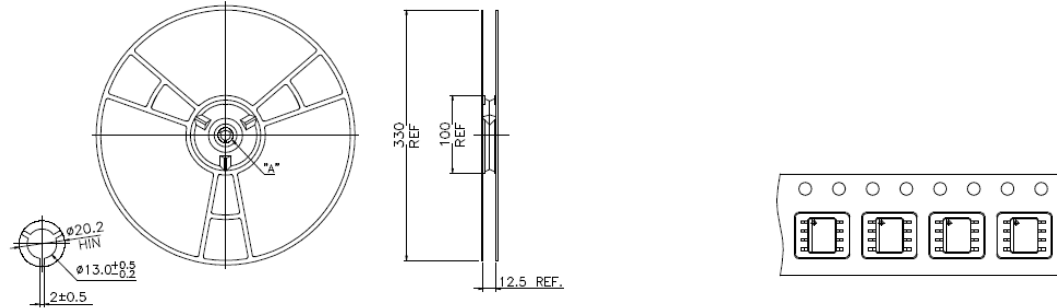
Power-ON
(V_{CC}=5V, V_{OUT}=3.3V, Load=0.8A)



❖ PACKAGE OUTLINES



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.35	1.60	1.75	0.053	0.063	0.069
A1	0.10	-	0.25	0.040	-	0.100
A2	1.30	1.45	1.50	0.051	0.057	0.059
C	0.19	0.20	0.25	0.0075	0.008	0.010
D	4.70	4.90	5.10	0.185	0.193	0.200
E	3.70	3.90	4.10	0.146	0.154	0.161
H	5.79	5.99	6.20	0.228	0.236	0.244
L	0.38	0.71	1.27	0.015	0.028	0.050
b	0.33	0.41	0.51	0.013	0.016	0.020
e	1.27 TYP.			0.050 TYP.		
y	-	-	0.10	-	-	0.004
θ	0°	-	8°	0°	-	8°

❖ CARRIER TAPE DIMENSION


\textcircled{A} $A_0 = 6.50$
 $B_0 = 5.20$
 $K_0 = 2.10$
 $K_1 = 1.70$

Notes:

- 10 sprocket hole pitch cumulative tolerance ± 0.2 mm
- Camber not to exceed 1mm in 100mm.
- Material: Anti-Static Black Advantek Polystyrene.
- A_0 and B_0 measured on a plane 0.3mm above the bottom of the pocket.
- K_0 measured from a plane on the inside bottom of the pocket to the top surface of the carrier.
- Pocket position relative to sprocket hole measured as true position of pocket, not pocket hole.