

# 52KHz, 2A PWM Buck DC/DC Converter

#### ❖ GENERAL DESCRIPTION

The AX3161 series are monolithic IC designed for a step-down DC/DC converter, and own the ability of driving a 2A load without additional transistor. It saves board space. The external shutdown function can be controlled by logic level and then come into standby mode. The internal compensation makes feedback control having good line and load regulation without external design. Regarding protected function, thermal shutdown is to prevent over temperature operating from damage, and current limit is against over current operating of the output switch. If current limit function occurs and V<sub>FB</sub> is down below 0.5V, the switching frequency will be reduced. An external compensation is easily to system stable; the low ESR output capacitor can be used.

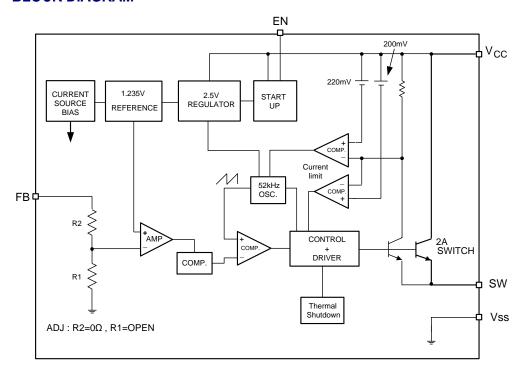
The AX3161 series operates at a switching frequency of 52KHz thus allow smaller sized filter components than what would be needed with lower frequency switching regulators. The output version included fixed 5V, 12V, and an adjustable type. The chips are available in standard TO263-5L packages.

#### ❖ FEATURES

- Output voltage: 5V and adjustable output version.
- Adjustable version output voltage range, 1.23V to 57V.
- 52KHz fixed switching frequency.
- Voltage mode non-synchronous PWM control.
- Thermal-shutdown and current-limit protection.
- ON/OFF shutdown control input.
- Short Circuit Protect (SCP).
- Operating voltage can be up to 60V.
- Output load current: 2A.
- TO263-5L Pb-Free packages.
- Low power standby mode.
- Built-in switching transistor on chip.
- RoHS and Halogen free compliance.

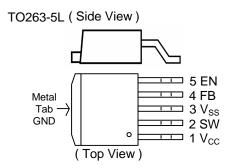


# **BLOCK DIAGRAM**



# **❖ PIN ASSIGNMENT**

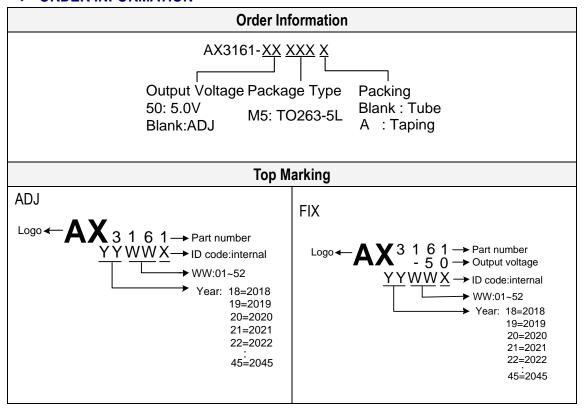
The package of AX3161 is TO263-5L; the pin assignment is given by:



Name	Description
Vcc	Operating voltage input
SW	Switching output
Vss	GND pin
FB	Output voltage feedback control
EN	ON/OFF Shutdown



#### ORDER INFORMATION



#### ❖ ABSOLUTE MAXIMUM RATINGS

Characteristics	Symbol	Rating	Unit	
Maximum Supply Voltage			+63	V
ON/OFF Pin Input Voltage		$V_{EN}$	-0.3 to 35	V
Feedback Pin Voltage		$V_{FB}$	-0.3 to 35	V
Output Voltage to Ground		Vout	-0.8	V
Power Dissipation Internally limited		PD	$(T_J-T_A)/\theta_{JA}$	W
Storage Temperature Range		T <sub>ST</sub>	-65 to +150	°C
Operating Junction Temperature Range		TJ	-40 to +125	°C
Operating Supply Voltage			+4.5 to +60	V
Thermal Resistance from Junction to case	TO263	θјс	3.5	°C/W
Thermal Resistance from Junction to ambient	TO263	$\theta_{JA}$	25	°C/W

Note 1:  $\theta_{JA}$  is measured with the PCB copper area (need connect to  $V_{SS}$  pins) of approximately 3 in<sup>2</sup> (Multi-layer).



# **\* ELECTRICAL CHARACTERISTICS**

(Unless otherwise specified, T<sub>A</sub>=25°C, V<sub>CC</sub>=12V.  $I_{LOAD}$  = 0.2A)

Charact	teristics	Symbol		Min	Тур	Max	Units
Quiescent Curre	nt	ΙQ	version force driver off		4	8	mA
Feedback bias c	urrent	I <sub>FB</sub>	(Adjustable version only)	-	-10	-50	nA
Shutdown supply	y Current	I <sub>SD</sub>	EN pin=5V V <sub>CC</sub> =60V	-	100	200	uA
Oscillator freque	ncy	Fosc		40	52	65	KHz
Oscillator freque	ncy of short	F <sub>SCP</sub>	(Adjustable) When V <sub>FB</sub> <0.5V	-	15	-	KHz
circuit protect		1 304	V <sub>FB</sub> =1.5V for Adjustable version force driver off V <sub>FB</sub> =1.3V (Adjustable version only) EN pin=5V V <sub>CC</sub> =60V  (Adjustable) When V <sub>FB</sub> <0.5V (Fixed)When < V <sub>OUT</sub> *40% V <sub>FB</sub> =0V force driver on V <sub>FB</sub> =1.5V for Adjustable version force driver off Pear current, No outside circly V <sub>FB</sub> =0V force driver on	-	15	-	KHz
Max. Duty Cycle	(ON)		V <sub>FB</sub> =0V force driver on	-	100	-	•
Min. Duty Cycle	(OFF)	DC	V <sub>FB</sub> =1.5V for Adjustable version force driver off	1	0	-	%
Current limit		I <sub>CL</sub>	Pear current, No outside circuit V FB=0V force driver on	2.3	ı	-	Α
Load Regulation	( $\triangle$ V <sub>OUT</sub> /V <sub>OUT</sub> )	Δ V <sub>OUT</sub>	I <sub>OUT</sub> = 0.2 to 2A	1	0.6	1.2	%
Saturation voltage	ge	V <sub>SAT</sub>	I <sub>OUT</sub> =2A, No outside circuit V <sub>FB</sub> =0V force driver on	1	1.2	1.4	V
SW pin leakage	SW Pin=0V	- I <sub>SW L</sub>	V <sub>FB</sub> =1.5V for Adjustable	1	-	-200	uA
current	SW Pin=-0.8V	ISWL		ı	-5	1	mA
EN pin logic inpu	ut threshold	V <sub>IL</sub>	Low (regulator ON)	ı	1 3	0.6	٧
voltage		V <sub>IH</sub>	(Adjustable) When V <sub>FB</sub> <0.5V (Fixed)When < V <sub>OUT</sub> *40%  V <sub>FB</sub> =0V force driver on  V <sub>FB</sub> =1.5V for Adjustable version force driver off  Pear current, No outside circular of the company of t	2.0	1.5	-	V
EN pin logic input current		I <sub>H</sub>	V <sub>EN</sub> =2.5V (OFF)	-	-0.1	-10	uA
EN pin input current		IL	V <sub>EN</sub> =0.5V (ON)	1	-0.01	-1	uA
Thermal shutdov	vn Temp	T <sub>SD</sub>		ı	150	-	°C

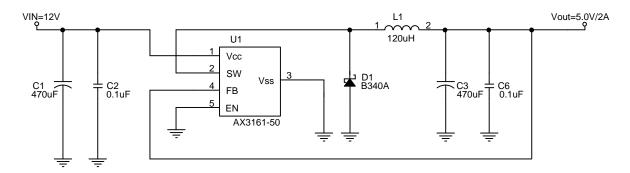


# **ELECTRICAL CHARACTERISTICS (CONTINUED)**

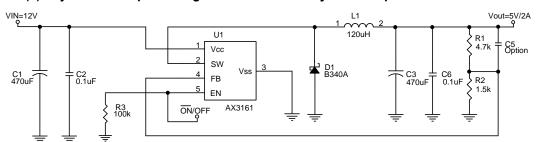
Version	Characteristics	Symbol	Conditions	Min	Тур	Max	Units
	Output Feedback	\/	I <sub>LOAD</sub> =0.2A V <sub>OUT</sub>	1 102	1.23	1.267	V
AX3161-ADJ	voltage	$V_{FB}$	programmed for 3.3V	1.193			
	Efficiency	η	V <sub>CC</sub> = 12V, I <sub>LOAD</sub> =2A	-	79	-	%
AX3161-5.0V	Output voltage	V <sub>OUT</sub>	I <sub>LOAD</sub> =0.2A	4.85	5.00	5.15	V
AA3101-3.0V	Efficiency	η	V <sub>CC</sub> = 12V, I <sub>LOAD</sub> =2A	-	83	-	%

# **❖ APPLICATION CIRCUIT**

# (1) Fixed Output Voltage Versions



# (2) Adjustable Output Voltage Version with Delayed Startup



$$V_{OUT} = V_{FB} \times (1 + \frac{R1}{R2}), V_{FB} = 1.23V, R2 = 0.47K \sim 3K$$

Table 1 Resistor select for output voltage setting

	·	
V <sub>OUT</sub>	R2	R1
3.3V	1.5K	2.5K
5V	1.5K	4.7K
12V	1.5K	13K



L1 recommend value (I <sub>OUT</sub> =2A,)						
Vout	V <sub>OUT</sub> 3.3V 5V 12V					
V <sub>IN</sub> =12V 100uH 120uH NA						
V <sub>IN</sub> =24V 120uH 150uH 200uH						
V <sub>IN</sub> =32~48V	120uH	150uH	300uH			

#### **❖ FUNCTION DESCRIPTION**

#### **Pin Functions**

Vcc

This is the positive input supply for the IC switching regulator. A suitable input bypass capacitor must be presented at this pin to minimize voltage transients and to supply the switching currents needed by the regulator.

#### Vss

Circuit ground.

#### SW

Internal switch. The voltage at this pin switches between (+ $V_{\text{CC}}$  -  $V_{\text{SAT}}$ ) and approximately – 0.5V, with a duty cycle of approximately V<sub>OUT</sub> / V<sub>CC</sub>. To minimize coupling to sensitive circuitry, the PC board copper area connected to this pin should be minimized.

#### **Feedback**

Senses the regulated output voltage to complete the feedback loop.

#### EN

Allows the switching regulator circuit to be shutdown using logic level signals thus dropping the total input supply current to approximately 100uA. Pulling this pin below a threshold voltage of approximately 1.3V turns the regulator on, and pulling this pin above 1.3V (up to a maximum of V<sub>CC</sub>) shuts the regulator down. If this shutdown feature is not needed, the EN pin can be wired to the ground pin.

### **Output Capacitor Selection**

The output capacitor is required to filter the output and provide regulator loop stability. The important capacitor parameters are; the 100 KHz Equivalent Series Resistance (ESR), the RMS ripples current rating, voltage rating, and capacitance value. For the output capacitor, the ESR value is the most important parameter. The ESR can be calculated from the following formula.

$$V_{RIPPLE} = \Delta I_L \times ESR = 0.4 \text{A x } 80 \text{m}\Omega = 32 \text{mV}$$

An aluminum electrolytic capacitor's ESR value is related to the capacitance and its voltage rating. In most case, higher voltage electrolytic capacitors have lower ESR values. Most of the time, capacitors with much higher voltage ratings may be needed to provide the low ESR values required for low output ripple voltage. It is recommended to replace this low ESR capacitor by using a  $470\mu$ F low ESR values <  $80m\Omega$ .

#### **Thermal Considerations**

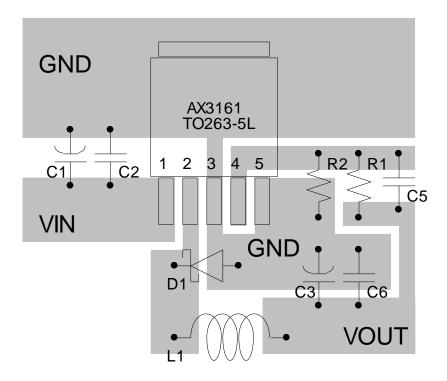
The data for these curves was taken with the AX3161 operating as a buck-switching regulator in an ambient temperature of 25°C (still air). These temperature increments are all approximate and are affected by many factors. Higher ambient temperatures require more heat sinker.

For the best thermal performance, wide copper traces and generous amounts of printed circuit board copper (need connect to the V<sub>SS</sub> pins) should be used in the board layout, (One exception is the SW(switch) pin, which should not have large areas of copper.) Large areas of copper provide the best transfer of heat (lower thermal resistance) to the surrounding air, and moving air lowers the thermal resistance even further.

Package thermal resistance and junction temperature increments are all approximate. The increments are affected by a lot of factors. Some of these factors include board size, shape, thickness, position, location, and even board temperature. Other factors are, trace width, total printed circuit copper area, copper thickness, single or double-sided, multi-layer board and the amount of solder on the board.

The effectiveness of the PC board to dissipate heat also depends on the size, quantity and spacing of other components on the board, as well as whether the surrounding air is still or moving. Furthermore, some of these components such as the catch diode will add heat to the PC board and the heat can vary as the input voltage changes. For the inductor, depending on the physical size, type of core material and the DC resistance, it could either act as a heat sink taking heat away from the board, or it could add heat to the board.

# **PCB Layout Recommendations**

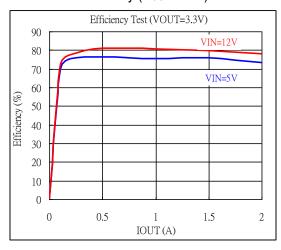


- 1. Connect the C1 and C2  $V_{IN}$  bypass capacitor next to the  $V_{CC}$  pin with a short return to the
- 2. Keep the SW traces as short as is practically possible as these carry high peak currents to decrease the EMI issue.
- 3. Carefully connect the noise sensitive signals such as FB as close to the IC as practically possible.

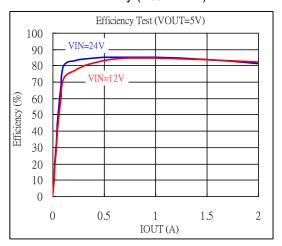


# TYPICAL CHARACTERISTICS

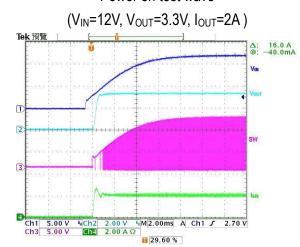
# Efficiency (Vout=3.3V)



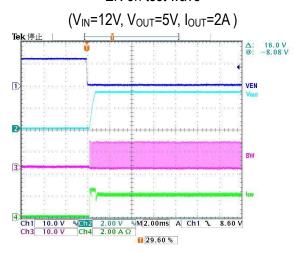
# Efficiency (V<sub>OUT</sub>=5.0V)



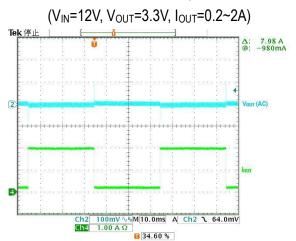
Power on test wave



EN on test wave



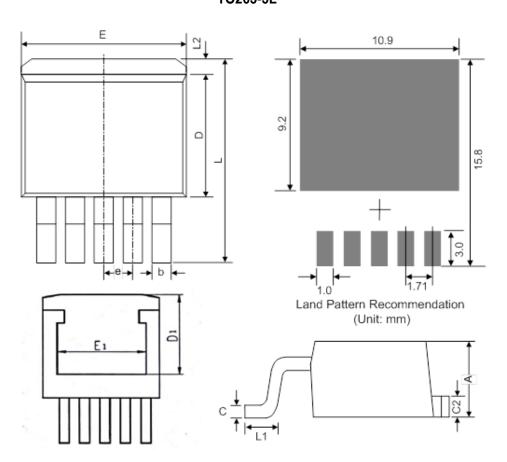
# Load Transient Response





# **PACKAGE OUTLINES**

# TO263-5L



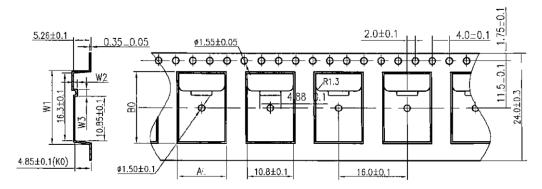
Symbol	Dime	Dimensions in Millimeters			Dimensions in Inches			
Symbol	Min.	Nom.	Max.	Min.	Nom.	Max.		
Α	4.06	4.45	4.83	0.16	0.175	0.19		
b	0.51	0.76	0.99	0.02	0.03	0.039		
С	0.38	0.56	0.74	0.015	0.022	0.029		
C2	1.14	1.4	1.65	0.045	0.055	0.065		
D	8.38	9.02	9.65	0.33	0.355	0.38		
D1	6.0	-	ı	0.24	ı	ı		
E	9,65	10.2	10.7	0.38	0.4	0.42		
E1	7.3	-	-	0.29	-	-		
е		1.70 BSC			0.067 BSC			
Ĺ	13.9	15	15.9	0.547	0.59	0.625		
L1	-	1.98	-	-	0.08	-		
L2	-	-	1.68	-	-	0.066		

Mold flash shall not exceed 0.005inch per side



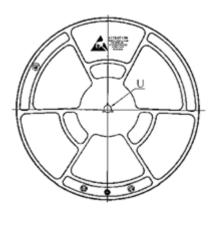
# ❖ Carrier tape dimension

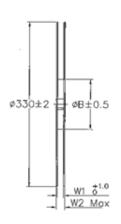
# TO263-5L

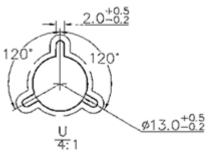


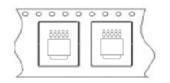
W1	17.2±0.1
W2	1.8±0.1
W3	0.85±0.1

(mm)









產品類別	載帶寬度	B(內徑)	W1	W2max
TO263	24	100	24.4	30.4
				(mm)

11/11