

N-Ch 100V Fast Switching MOSFETs

❖ GENERAL DESCRIPTION

The AM6008 is the high cell density trench N-ch MOSFETs, which provide excellent $R_{DS(ON)}$ and gate charge for most of the Synchronous Rectification for AC/DC Quick Charger.

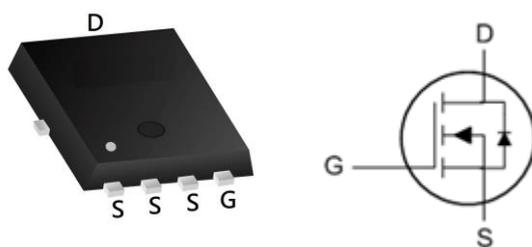
❖ FEATURES

- 100% EAS Guaranteed
- Low $R_{DS(ON)}$
- Low Gate Charge
- RoHS and Halogen free compliant

Product Summary

BVDSS	$R_{DS(ON)}$	ID
100V	8m Ω	48A

PRPAK5X6 Pin configuration



❖ ELECTRICAL CHARACTERISTICS

 ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	100	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=13.5A$	---	6.6	8	m Ω
	Static Drain-Source On-Resistance ²	$V_{GS}=4.5V, I_D=11.5A$	---	8.7	10.5	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	---	2.3	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=80V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	---	---	1	uA
		$V_{DS}=80V, V_{GS}=0V, T_J=55^{\circ}\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=5V, I_D=20A$	---	85	---	S
Q_g	Total Gate Charge (10V)	$V_{DS}=50V, V_{GS}=10V, I_D=13.5A$	---	45	---	nC
Q_g	Total Gate Charge (4.5V)		---	19.3	---	
Q_{gs}	Gate-Source Charge		---	9.5	---	
Q_{gd}	Gate-Drain Charge		---	4.8	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=50V, V_{GS}=10V, R_G=3, I_D=13.5A$	---	10	---	ns
T_r	Rise Time		---	6.5	---	
$T_{d(off)}$	Turn-Off Delay Time		---	45	---	
T_f	Fall Time		---	7.5	---	
C_{iss}	Input Capacitance	$V_{DS}=50V, V_{GS}=0V, f=1\text{MHz}$	---	3320	---	pF
C_{oss}	Output Capacitance		---	605	---	
C_{rss}	Reverse Transfer Capacitance		---	20	---	

❖ Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current ^{1,5,6}	$V_G=V_D=0V$, Force Current	---	---	48	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^{\circ}\text{C}$	---	---	1.1	V
t_{rr}	Reverse Recovery Time	$I_F=13.5A, di/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	33	---	nS
Q_{rr}	Reverse Recovery Charge		---	150	---	nC

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating. The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.3mH, I_{AS}=35A$
- The power dissipation is limited by 150 $^{\circ}\text{C}$ junction temperature
- The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.
- The maximum current rating is package limited.

❖ TYPICAL CHARACTERISTICS

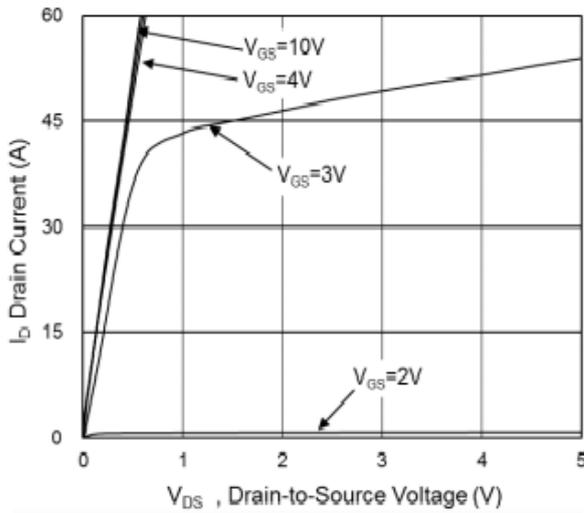


Fig.1 Typical Output Characteristics

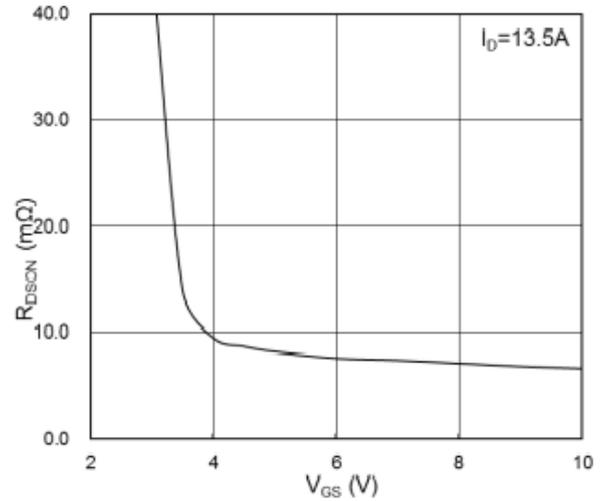


Fig.2 On-Resistance vs G-S Voltage

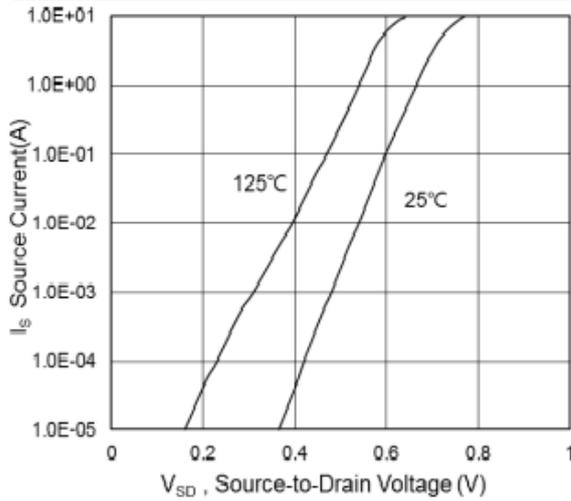


Fig.3 Source-Drain Forward Characteristics

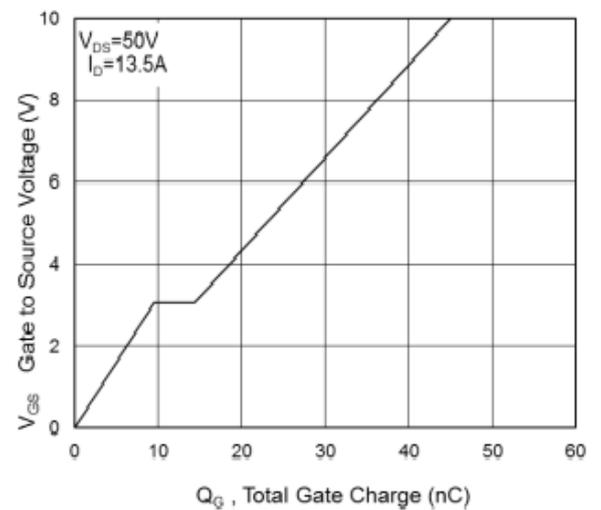
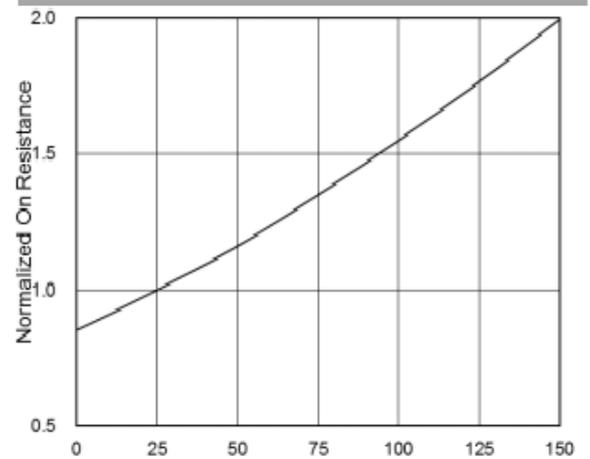
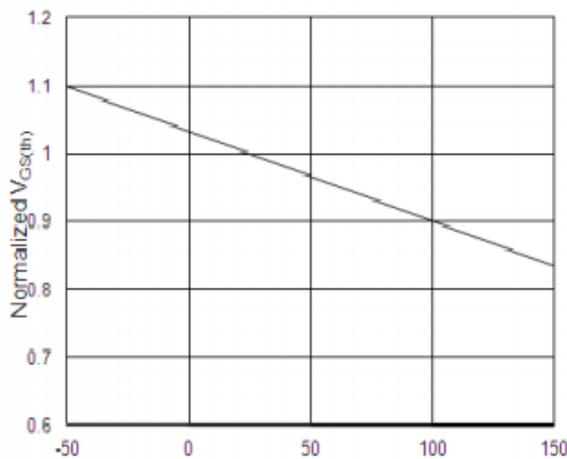


Fig.4 Gate-Charge Characteristics



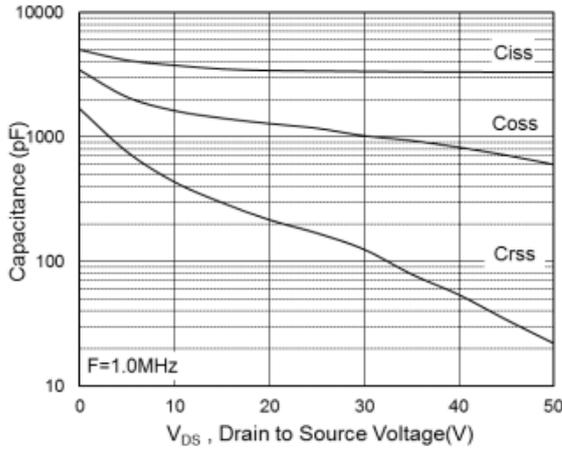


Fig.7 Capacitance

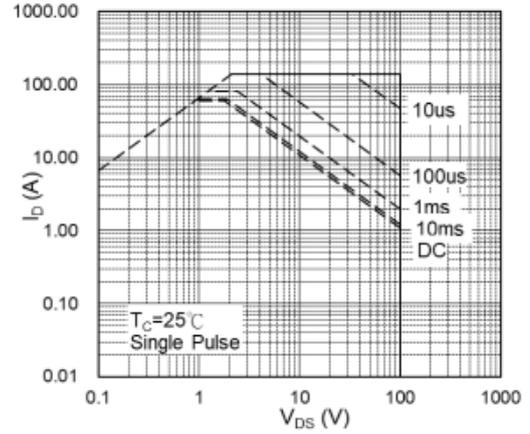


Fig.8 Safe Operating Area

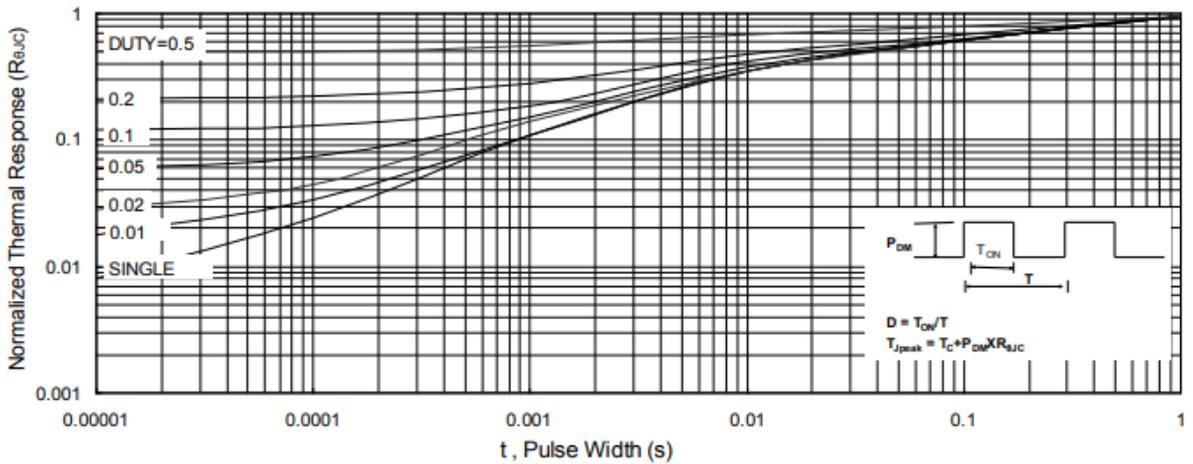


Fig.9 Normalized Maximum Transient Thermal Impedance

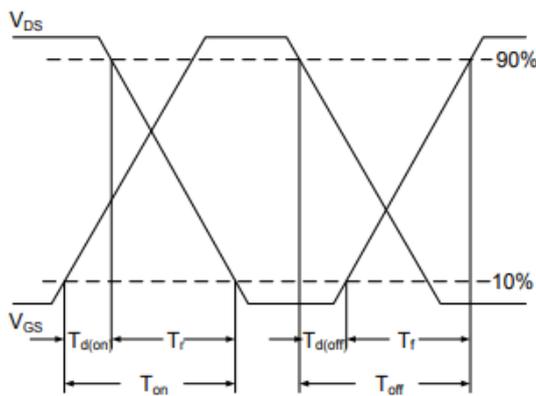


Fig.10 Switching Time Waveform

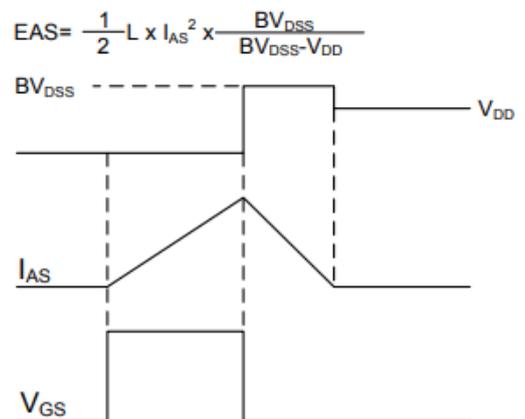
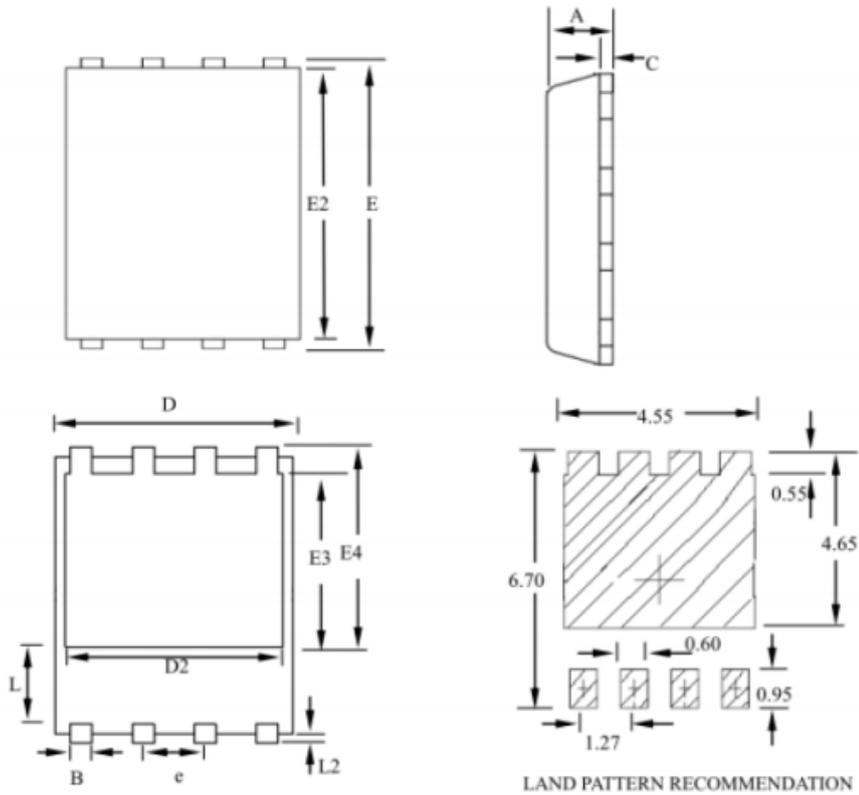


Fig.11 Unclamped Inductive Switching Waveform

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

❖ PACKAGE OUTLINES

PRPAK5X6 Package Outline Dimensions



SYMBOLS	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	--	1.20	0.031	--	0.047
B	0.30	--	0.51	0.012	--	0.020
C	0.15	--	0.35	0.006	--	0.014
D	4.80	--	5.30	0.189	--	0.209
D2	3.61	--	4.35	0.142	--	0.171
E	5.90	--	6.35	0.232	--	0.250
E2	5.42	--	5.90	0.213	--	0.232
E3	3.23	--	3.90	0.127	--	0.154
E4	3.69	--	4.55	0.145	--	0.179
L	0.61	--	1.80	0.024	--	0.071
L2	0.05	--	0.36	0.002	--	0.014
e	--	1.27	--	--	0.050	--